

Performance Analysis of Power Electronic Distribution Transformer using Adaptive Pi-Fuzzy Logic Controller



Vishnupriya K., Ramya G.

Abstract: This paper enlightens the analysis of distributed transformer using novel controller. The transformer initiates power distribution by stepping up or down depends upon the need. In accordance with power quality issues, performance, dynamic response, power factor correction the normalized transformer is inconvenient. Replacing the uncertainties by introducing a power electronic distribution transformer (PET) with PI controlled adaptive fuzzy logic control (APIFC). Moreover the occurrence of breakdown voltage and stress in conventional method, this method becoming more popular. This transformer become sensitive to dealt with swell, sag, voltage regulation, open circuit and short circuit fault. The specified rectifier also does power factor correction. The strategic PI controller with adaptive fuzzy logic control technique is established to respond sudden change or variation occurs in load. The fuzzy logic controller (FLC) posses five set of rules and PI controller adapt with one among five set of rules. This makes the controller to adjust with variation and improves power quality. The simulation is carried out using MatLab-Simulink.

Keywords: Power Electronic distribution transformer (PET), Proportional Integral control (PI), Fuzzy Logic controller (FLC), Adaptive fuzzy proportional integral control (APIFC)

I. INTRODUCTION

The power transformer has sustainable for power distribution system. The conventional transformer becoming huge in size, suffers from harmonics, internal core losses, etc. Normally a transformer undergoes hysteresis loss, eddy current loss and dielectric loss [1]-[5]. When non-linear load is attached with conventional transformer, it undertakes severe heat and losses in transformer winding. So it could not have high power control capacity. As from the above record it is visualized that the usual transformer is not well suited for distribution system. The power electronic distribution transformer has been considered as a solution for distribution

system. It promotes long transmission with unity power factor and power correction. The emerging technology is responsible for rectifying losses occurs within the system. It relieves system from dielectric loss, voltage fluctuation, and harmonic distortion. Moreover that, this system imparts huge power transmission without any difficulties (i.e.) sag, swell, open circuit and short circuit fault. Normally a power electronic distribution transformer posses power semiconducting devices at both front end and back end respectively. The PET employs two strategies: 1) without DC Link capacitor, 2) with DC link capacitor. The power distribution system cannot have dc link capacitor; so the construction becomes simple, less weight, less stress factor [6]-[8]. But this type does not contribute towards power conversion and power improvement. In second approach the DC link can have the ability to perform power factor correction and voltage regulation. This method has becoming more convenient for voltage regulation. In our proposed system the dc link capacitor included. The size of the transformer become compact, more efficient, draws attention immediately when disturbance occurs. The fuzzy logic associated with PI controller has been included with PET to obtain better performance. The adaptive fuzzy logic controller contains five rules [9]-[12]. The PI control technique would not have the ability to acts upon the changes and variation in voltage. Hence the quality of power may fail. Then we introduce fuzzy logic control with PI to accept the changes occur in a system; also provide a better solution to the system. The proposed topology is further divided into three sections namely: input stage, isolation stage, output stage shown in Fig.1 In input stage the three phase supply is converter into dc supply. In output stage three phase present within it. It converts the dc into ac voltage efficiently. More or less from the previous sections are corrected in this section. Finally the required output is fed to the load without any fault. Further power factor correction and power regulation is also possible in this proposed topology. Apart from control technique, these topology posses a unique solution for power factor correction. Most of the rectifiers perform rectification only. But this can acts as power factor correct ion converter also. The Vienna rectifier brings power from the AC supply. It transforms ac into dc voltage and capacitor present in it can perform power factor correction.

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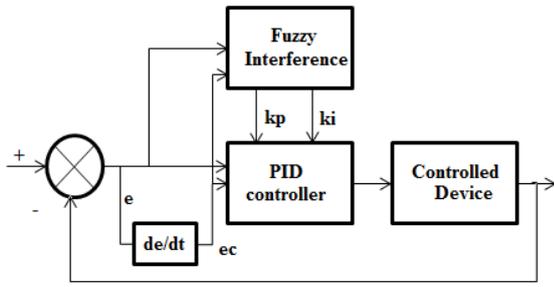


Fig.1. Schematic block of proposed system

Therefore the output power has low ripple and it is at acceptable rate. It does not create harness to the converter portion. If there is any disturbance within input voltage the rectifier corrects and do not make trouble in converter portion. A multi-fed power electronic transformer has the ability to function with active power from the feeder and performs lossless power improvement with ac-dc and dc-ac conversion topology. It enrols flexibility and smart grid concept. These topology also governs power quality is represented. The protection of transformer under various circumstances like low frequency disturbance, grounding and over voltage is proposed. It is essential to provide grounding when the transformer faces low frequency during transmission. The medium voltage stress generated arc suspension coil is caught and negative sequence current compensation is chosen as remedy to this problem. By injecting zero sequence voltage, the arc suspension impedance also adjusted. Hybrid AC-DC conversion with multiple voltage level evolves cascaded multiport power electronic transformer enable reliability and redundancy. Both AC/DC conversion and power electronic transformer reliability and redundancy are analysed under various operating conditions. In paper, medium and high voltage applications, smart grid power distribution employ dc power electronic distribution transformer. To locate the faulty section quickly and respond well with fewer switches and high frequency transformer without any change in line voltage; does not require any impedance injection. The solid state transformer (power electronic transformer) interfaced ac and dc distribution grid using dual active bridge converter, high frequency transformer, and multilevel cascaded ac/dc rectifier is conveyed. This topology makes correction upon unbalanced dc link voltage and semiconductor stress causing over voltage or over current. A single phase dq model, rectifier and real power flowing through dual active bridge converter are also established [13]-[17]. The adaptive fuzzy inter combined with PI control accepts the variation in load, ac grid, variable operating conditions is proposed. Instead of dc link capacitor a high frequency ac link capacitor adjust with variable frequency and pulse width modulation (PWM) from a source. At output stage the leakage energy from isolation stage is recovered. This deals with the quality and controllability using high frequency transformer, centralized ac-dc converter, high frequency transformer using less switches. A fuzzy logic control based solid state transformer ignore fault within system load line and it takes action upon variable operation of converter with respond to input voltage. Rectifier performs power factor correction; also discrete space vector modulation controls rotational motion of motor,

ripple, noise.

II. PROPOSED SYSTEM

The schematic circuit of proposed system is shown in Fig.2. The proposed system has three categories: an input, isolation and output stage. The input stage acts as rectifier circuit where ac in converted into dc signals. The first stage rectifier is used to improve the power factor and regulate the supply voltage. The second stage consists of two half bridge converter connected to high frequency transformer. In first half bridge the converter generated high frequency square wave voltage and then stepped down to dc voltage. The final stage is dc-ac inverter converter where the dc signals s converted to ac to feed the load. The mathematical modelling of abc frame of three level PWM rectifier is

$$e_a = L \frac{di_a}{dt} + Ri_a + s_{a1}v_{dc1} - s_{a2}v_{dc2} + u_{n0} \quad (1)$$

$$e_b = L \frac{di_b}{dt} + Ri_b + s_{b1}v_{dc1} - s_{b2}v_{dc2} + u_{n0} \quad (2)$$

$$e_c = L \frac{di_c}{dt} + Ri_c + s_{c1}v_{dc1} - s_{c2}v_{dc2} + u_{n0} \quad (3)$$

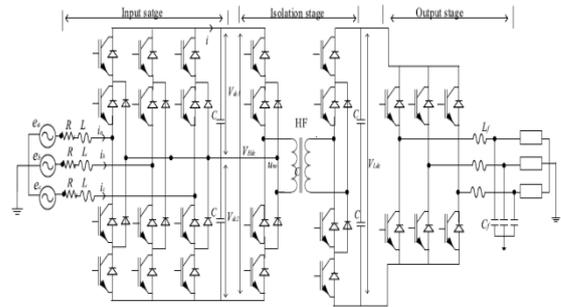


Fig.2. Proposed topology

The dc capacitor voltages are

$$C_d \frac{dv_{c1}}{dt} = s_{a1}i_a + s_{b1}i_b + s_{c1}i_c - i \quad (4)$$

$$C_d \frac{dv_{c2}}{dt} = -s_{a2}i_a - s_{b2}i_b + s_{c2}i_c - i \quad (5)$$

Where

e_a, e_b, e_c - ac input voltages

L - Inductance

R - Resistance

V_{dc1} and V_{dc2} - DC-link voltage,

i_i - rectifier output current,

C_d - DC-link capacitor

$S_a, S_b,$ and S_c - switching functions

u_{no} - voltage between the grid neutral point

Modelling and design of controller is implemented by converting abc variables into d-q frame. The rectifier circuit with synchronous rotation d-q coordinate can be represented as

$$L \frac{di_d}{dt} = -Ri_d + \omega Li_q - s_{d1}v_{dc} + s_{d2}v_{dc} + e_q \quad (6)$$

$$L \frac{di_q}{dt} = -Ri_q + \omega Li_d - s_{q1}v_{dc} + s_{q2}v_{dc} + e_q \quad (7)$$

| | |
|---------------------------|---------|
| V_0 | 0.415kV |
| L | 0.25H |
| R | 0.07Ω |
| C_{dc1}, C_{dc2} (high) | 20μF |
| V_{hdc} | 20kV |
| C_{dc1}, C_{dc2} (low) | 30mF |
| V_{Ldc} | 0.8kV |
| F_{sw} | 10kHz |
| L_f | 4mH |
| R_f | 100kW |

III. RESULT AND DISCUSSION

The simulation is carried out using MatLab platform. The performance analysis is carried based on voltage sag and harmonic distortion

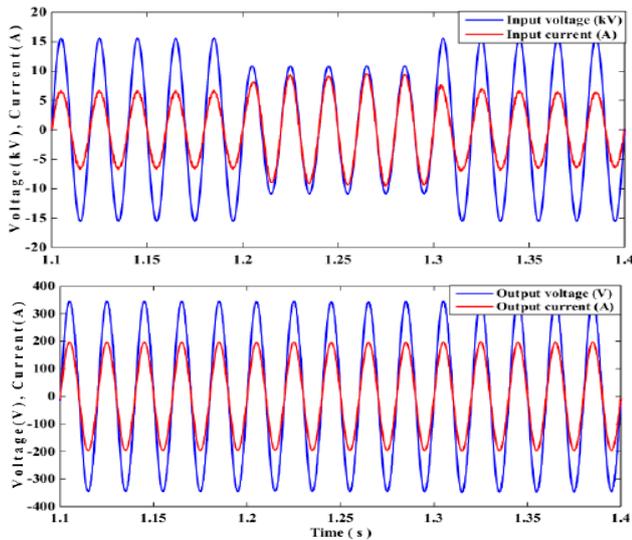


Fig.6. Input-output voltage and current response with API-FLC

A deviation at voltage is introduced at time instant 1.2 to 1.3 s as shown in Fig.6. Due to adaptive fuzzy PI controller unit the voltage deviation is compensated as shown in Fig. 6.

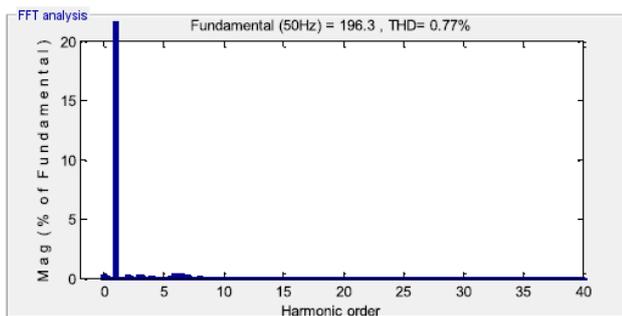


Fig.7. Output current THD with PI controller

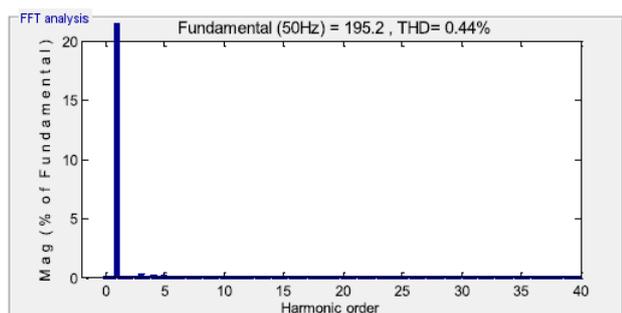


Fig.8. Output current THD with proposed controller

The THD for both the conventional PI and adaptive FLC controller are shown in Fig.7 & 8. The PI controller has an output current THD of 0.77% and adaptive FLC is 0.44% having lower harmonics and improved power quality.

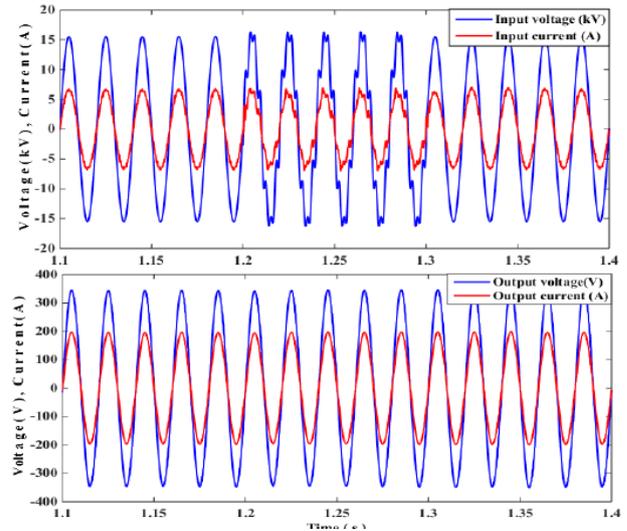


Fig.9. Input-output voltage and current response under voltage harmonics

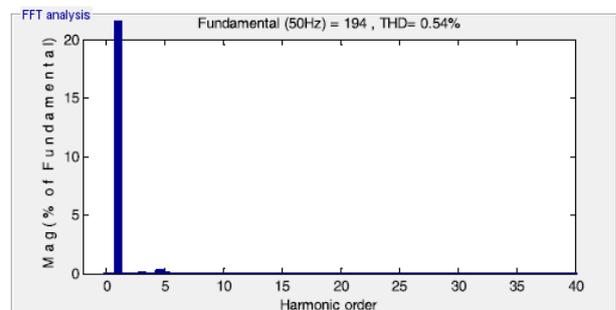
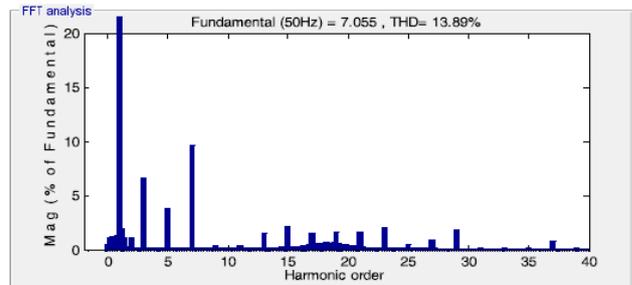


Fig.10. Output current THD with API-FLC

The voltage harmonics is applied at primary side of transformer having THD of 13.89% as shown in Fig. 9. And the transformer secondary output current THD of 0.54% is shown in Fig.10. The THD percentage satisfies the IEEE standard which is less than 5%. From the obtained simulation results it is inferred that the proposed system has improved power quality and less harmonics.

IV. CONCLUSION

In this research work a novel adaptive FLC based PI controller is implemented. The proposed controller unit automatically adjusts the gain value based on the deviation at the output side and the performance analysis is carried out based on harmonics and voltage compensation under dynamic condition.



Based the obtained simulation results the proposed controller has less harmonics and better performance under dynamic state compared to conventional PI controller.

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