



# Substrate Integrated Waveguide Slot Array antenna for Broadband Applications

Rekha S, S Ramani

**Abstract :** This paper presents a design of substrate integrated waveguide slot array antenna for X and Ku band applications. This is a low weight and simple profile antenna covering a vast range of frequencies. A substrate integrated waveguide is formed by two rows of metallized cylinders connecting upper and bottom layer of substrate. On the metallic plate, slot arrays are etched in a periodic fashion to achieve broad bandwidth. The SIW is excited by the microstrip feeding through a tapered transition to achieve smooth flow of current. The simulated result shows that -10 dB return loss of the proposed antenna is -35 dB, -29 dB and -22 dB at 11.1 GHz, 12.7GHz and 17.6 GHz respectively. The simulated bandwidth of the design is 7.6 GHz from 10.4-18 GHz which covers X band partially and Ku band. The gain of the antenna is about 6 dB. The proposed design is simulated using 3D full wave simulator named Ansys HFSS.

**Keywords :** Substrate integrated waveguide, slot arrays, broad bandwidth, X band and Ku band applications.

## I. INTRODUCTION

Substrate integrated waveguide (SIW) is an emerging technology for the design of microwave circuits, antennas and cost-effective components. Traditional transmission lines like microstrip and coaxial lines suffer conductor and radiation losses at high frequencies. The metallic waveguide has good performance at high frequency but it is bulky and costly. In order to balance the cost and performance, SIW was developed. This SIW has three layers namely top metallic layer, bottom metallic layer and intermediate dielectric substrate. Metallic cylinders are inserted between top and bottom metallic conductors. This SIW has the merits of high power handling capacity, low radiation losses and less fabrication cost. The structure of SIW is similar to dielectric filled rectangular waveguide [1]. The slot is etched on the top surface of the SIW to radiate the electromagnetic waves. Different shapes of slot [2-3] are etched to increase the bandwidth since conventional rectangular slot is a narrow band [4]. The slot arrays are introduced to create resonance at different frequencies and the bandwidth is extended for different microwave applications [5-7]. Such slot arrays on SIW is also employed to design passive devices like couplers

[8], filters [9], etc. In this paper, slot arrays are etched on the top metallic layer of SIW and they finds its application in X and Ku bands. The SIW is fed through microstrip feeding on both the ends via tapered transition.

## II. DESIGN EQUATIONS OF SIW

The proposed design is built on the substrate namely Rogers RT/Duroid 5880 having  $\epsilon_r$  as 2.2,  $\mu_r$  as 1 and having dielectric loss tangent of 0.0009. The top and bottom geometry is shown in figure 1 and 2. The dimension of the proposed design is mentioned in Table 1. The overall dimension of the design is 40 x 14.6 x 0.762 mm<sup>3</sup>. The resonant frequency of the SIW slot array antenna is given in equation (1).

$$f_{r(TE_{m,n,q})} = \frac{c}{2\sqrt{\epsilon_r}} \sqrt{\left(\frac{m}{a}\right)^2 + \left(\frac{n}{b}\right)^2 + \left(\frac{q}{c}\right)^2} \quad (1)$$

where,

c - velocity of light in free space,

$\epsilon_r$  - relative permittivity of the substrate,

a,b,c - width, height and length of SIW,

m,n,p - positive integers.

To minimize leakage between the consecutive metallized cylinders, equation (2) and (3) are maintained.

$$d/s \geq 0.1 \quad (2)$$

$$d/\lambda_0 \leq 0.1 \quad (3)$$

where, d is diameter of the metallized cylinder, s is center to center distance between the successive cylinders and  $\lambda_0$  is free space wavelength. The slots of a particular shape are etched in the definite intervals on the top conductor.

These slots are placed on the dense current distributed region having a distance of  $\lambda/4$  from the tapered edge. The distance of  $\lambda/4$  is maintained in order to create constructive interference of radiated waves.

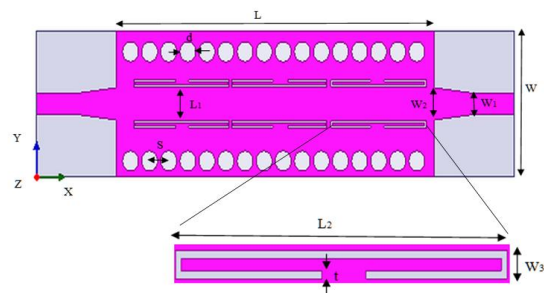


Fig. 1. Top view of the proposed SIW slot array antenna

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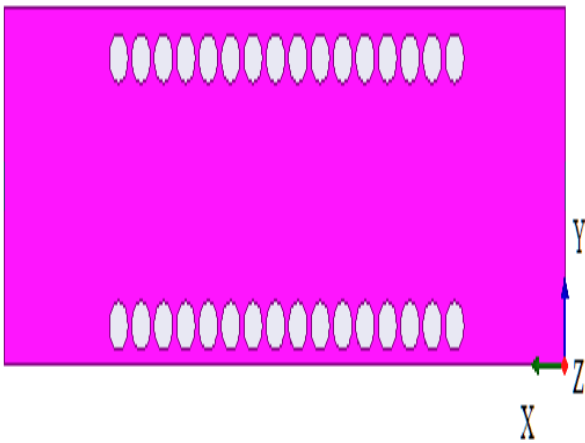


Fig. 2. Rear view of the proposed SIW slot array antenna

Table- II: Dimensions of the proposed design

S. No	Parameters	Dimensions (in mm)
1.	L x W x H	40 x 14.6 x 0.762
2.	S	1.3
3.	d	2
4.	L <sub>1</sub>	3.5
5.	W <sub>1</sub>	2
6.	W <sub>2</sub>	3
7.	L <sub>2</sub>	11.8
8.	W <sub>3</sub>	0.6
9.	t	0.2

III. RESULTS AND DISCUSSIONS

The simulated proposed design has strong resonances at 11.1 GHz, 12.7 GHz and 17.6 GHz and their return losses are -35 dB, -29 dB and -21 dB respectively. It shows a broad bandwidth from 10.4 to 18 GHz that occupies the X band (upper half) and Ku frequency band. The simulated return loss and insertion loss is shown in figure 3 in which the insertion loss is around -2 dB (acceptable). Figure 4 shows that the voltage standing wave ratio (VSWR) is less than 2 for the operating bandwidth.

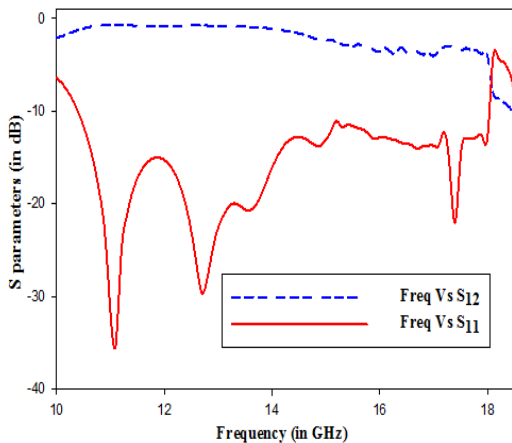


Fig. 3. Simulated return loss and insertion loss of proposed SIW slot array

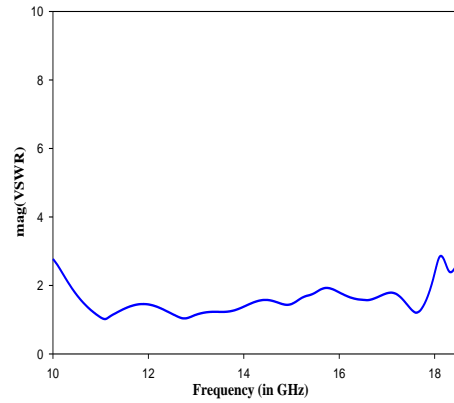


Fig. 4. Simulated VSWR of proposed SIW slot array

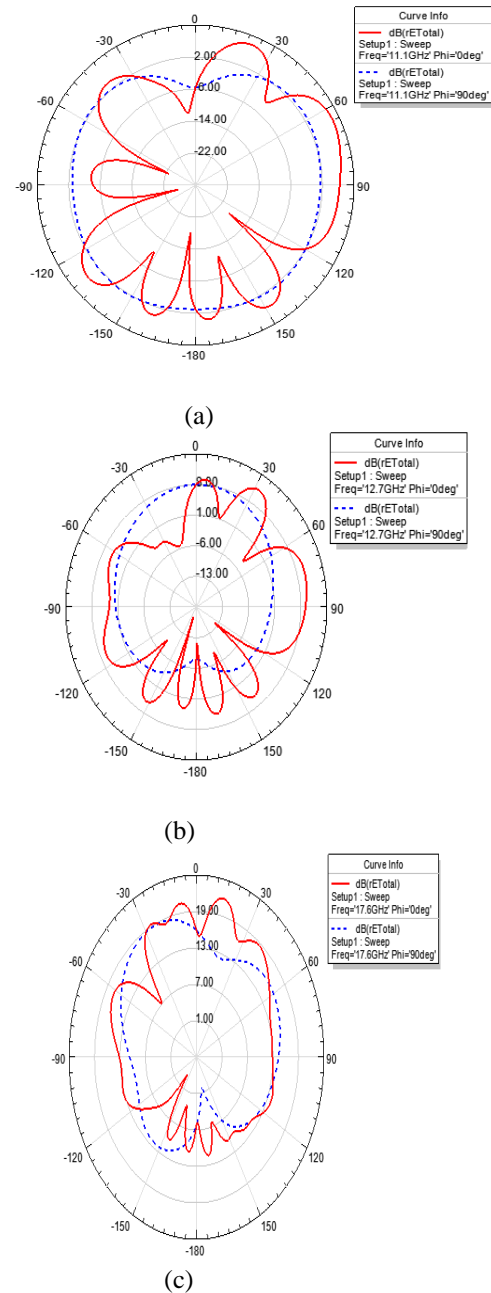


Fig. 5. Simulated radiation pattern of proposed SIW slot array

At resonance frequencies (a) 11.1 GHz (b) 12.7 GHz (c) 17.6 GHz and The radiation pattern for resonant frequencies is presented in figure 5. It is plotted for the resonant frequencies of 11.1 GHz, 12.7 GHz and 17.6 GHz. The figure shows plotting of electric field and magnetic field pattern. The antenna is radiating sufficiently at their resonant frequencies. The radiation pattern shows that it is a quasi omni-directional antenna. Figure 6 shows simulated E field distribution at peak resonant frequency of 11.1 GHz. The figure shows that the radiation is maximum around the slots which is indicated by intensity of the colours. The peak gain of the proposed antenna is around 6 dB. The radiation efficiency of the design is about 75%.

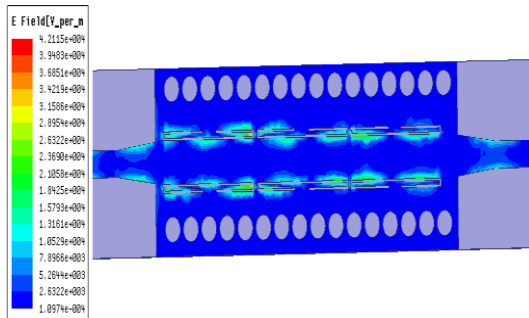


Fig. 6. Simulated E field distribution of SIW slot array at peak resonant frequency of 11.1 GHz

#### IV. CONCLUSION

The substrate integrated waveguide slot array antenna is presented in this work. The antenna is designed for X band and Ku band applications. The profile of the antenna is maintained simple with use of substrate integrated waveguide technology. The antenna is fed by a microstrip feeding using a tapered section on both its sides in order to achieve smooth flow of current distribution. The slot array etched on the top conducting layer of the antenna. The etching of slots has introduced multiple resonances at different frequencies. The length of the slot is about half of the wavelength and the distance of the slot from the edge of excitation is about quarter of the wavelength. The length and distance are maintained in terms of wavelength to create constructive interference of the waves. Then, the multiple resonances are adjusted by varying the electrical lengths of the slot to create a broad bandwidth. These resonances are extended by introducing small resonances between peak resonances and broad bandwidth is achieved. The antenna exhibits wideband characteristics from 10.4 GHz to 18 GHz which covers both X band and Ku band frequencies. It gives a nominal gain of about 6dB at resonances. The radiation efficiency of the antenna is 75%.

#### REFERENCES

1. G Cheng, Y. J. (2015) "Substrate Integrated Antennas and Arrays", First Edition, CRC Press, ISBN 9781498714532.
2. Rekha, S. and Nesasudha, M. (2017) "Rear end planar slot antenna of a substrate integrated waveguide cavity fed by grounded coplanar waveguide", First IEEE international conference on Recent Advances in Aerospace Engineering, pp. 1-3.
3. Mukherjee, S. and Biswas, A. (2016) "Design of self-diplexing substrate integrated waveguide cavity backed slot antenna", IEEE Antennas and Wireless Propagation Letters 15, 1775-1778.

4. Luo, G. Q., Hu, Z. F., Dong, L. X. and Sun, L. L. (2008) "Planar slot antenna backed by substrate integrated waveguide cavity", IEEE Antennas Wireless Propagation Letters 7(1), 236-239.
5. Li, W., Xu, K. D., Tang, X., Yang, Y., Liu, Y., and Liu, Q. H. (2017) "Substrate Integrated Waveguide Cavity-Backed Slot Array Antenna Using High-Order Radiation Modes for Dual-Band Applications in SKS-Band," in IEEE Transactions on Antennas and Propagation 65(9), 4556-4565.
6. Kumar. K. and Dwari. S. (2017) "Substrate Integrated Waveguide Cavity-Backed Self-Triplexing Slot Antenna," in IEEE Antennas and Wireless Propagation Letters 16, 3249-3252.
7. Rekha. S. and Nesasudha. M. (2018) "Design of Substrate Integrated Waveguide Slot Array for Wideband Applications", second International Conference on Research Trends in Engineering, Applied Science and Management, 251-254
8. Liu, B., Hong, W., Wang, Y., Lai, Q. and Wu, K. (2007) "Half Mode Substrate Integrated Waveguide (HMSIW) 3-dB Coupler," in IEEE Microwave and Wireless Components Letters 17(1), pp. 22-24.
9. Wang, Y., et al. (2007), "Half Mode Substrate Integrated Waveguide (HMSIW) Bandpass Filter," in IEEE Microwave and Wireless Components Letters 17(4), 265-267.

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