

Design of Coplanar Circuit Switching Network in Quantum Dot Cellular Automata



A.Chandrasekaran, K.Senthil kumar, K.Hemalatha, K.STamilselvan, P.Umarani

Abstract: Quantum dot Cellular Automata (QCA) is the alternative technology to the classic CMOS technology since it is going to attain a road block in reducing power consumption and increase speed of the digital circuits. Circuit switching network is the basic component in order to transmit input signal among the different users within the communication network. A novel crossbar switch is proposed in this paper to design this communication network. The basic building blocks of the proposed circuit Switching network are Crossbar switch, Multiplexer and Demultiplexer. Multilayer QCA cells are almost impossible when compared to the fabrication feasibility of the single layer design. So this design is achieved in single layer. Circuit switching network is designed and compared with existing one using QCA Designer 2.0.3. The designs are verified through matching up with truth tables.

Keywords : QCA, Crossbar switch, Circuit Switching network,

I. INTRODUCTION

Quantum – dot cellular automata is one of the most promising solution in reducing area , power consumption and in increasing speed of the digital circuits. Binary values ‘0’ and ‘1’ can be represented with the help of the position of electrons. Basic logic gates in the QCA are majority gate and Inverter [7-9]. Some of the advantages of QCA technology over CMOS technology are:

1. CMOS transistors cannot be scaled beyond a particular size any further
2. CMOS interconnects have not grown to Work as fast as the device them
3. Power consumption due to leakage current is significant in the CMOS technology and has no cent percent solution till date [10-14].

Optimistic assumptions indicate that the technology has the potential to break the terahertz barrier. It has low power

consumption. Electrons are the main elements in QCA to perform operations and to propagate informations. Dr. Craig Lent at Notre Dame titled Quantum-dot cellular automata, which is a new form of logic gates using quantum dots. The logic unit in QCA is the QCA cell. It is composed of four quantum dots [15-17].

Circuit switching network is the basic component in order to transmit input signal among the different users within the communication network. The main building blocks of the proposed circuit switching network are crossbar switch, multiplexer and demultiplexer. Multilayer QCA cells are almost impossible when compared to the fabrication feasibility of the single layer design. So this design is achieved in single layer. The implementation and simulation of the proposed circuit switched network is achieved through QCA designer 2.0.3 tool. The designs are verified through matching up with truth tables.

II. RELATED WORKS

There are only few works reported to look into the application of QCA in designing the architecture of nanocommunication. In [1] Circuit switching network is achieved in a single layer and it is evaluated in terms of area, latency and logic gates. It uses 382 cells and of area $1.02 \mu m^2$. In [2], a novel structure for the $2 \times 1, 4 \times 1, 8 \times 1$ multiplexer are designed and it does not follow any Boolean function. The proposed structures performance is high in terms of area, latency and power consumption. In [3], a new and efficient architecture for $2 \times 1, 4 \times 1, 8 \times 1$ multiplexer are implemented with the coplanar crossover. In [4], a reversible 2×2 crossbar switch is designed and its impact on information processing is analysed. Parameters like density, logic gates and latency are considered in order to confirm faster operating speed and high device density. In [5], a novel design of 2×1 multiplexer is proposed to reduce delay and area.

III. QCA BASED CROSSBAR SWITCH

Crossbar switch can be achieved using 2×1 multiplexer. It is a logic circuit that performs switching operation by connecting multiple input to multiple output line. It has multiple input and multiple output line that is represented as $m \times n$ crossbar switch. It forms the crossed like pattern because of the interconnection between input and output lines. The simultaneous connections do not affect the communication between other input and output lines. The simple example is 2×2 crossbar switch as shown in Fig. 1. It has two input lines A, B and two output lines C, D. S is the control signal (i.e., switch).

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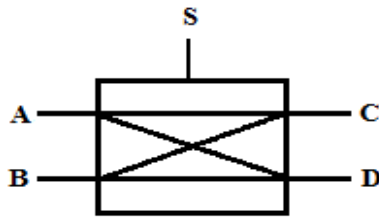


Fig. 1. Crossbar switch 2x2

In this 2x2 crossbar switch, if the control signal is zero (S=0), A is connected with C and B is connected with D. Similarly, if the control signal is one (S=1), A is connected with D and B is connected with C. It is explored in truth table as shown in Table 1. 2x2 crossbar switch circuit diagram is shown in Fig. 2 It is described through equations as follows, If S=0,

$$A=C \quad (1)$$

$$B=D \quad (2)$$

If S=1,

$$A=D \quad (3)$$

$$B=C \quad (4)$$

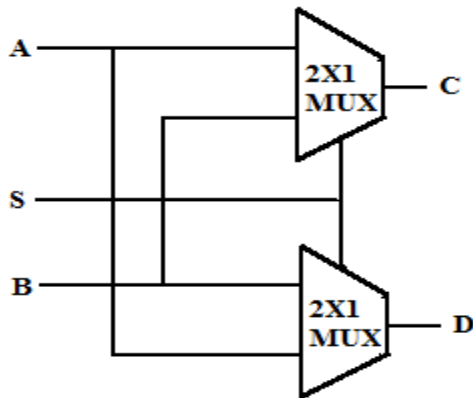


Fig. 2 Crossbar switch 2x2 (Circuit diagram)

The first 2x1 MUX generates output expression for C and second 2x1 MUX generates output expression for D. Schematic diagram for 2x2 crossbar switch is shown in Fig. 3.

Table 1 Truth table of 2x2 crossbar switch

Input			Output	
Control input (S)	A	B	C	D
0	0	0	0	0
0	0	1	0	1
0	1	0	1	0
0	1	1	1	1
1	0	0	0	0
1	0	1	1	0
1	1	0	0	1
1	1	1	1	1

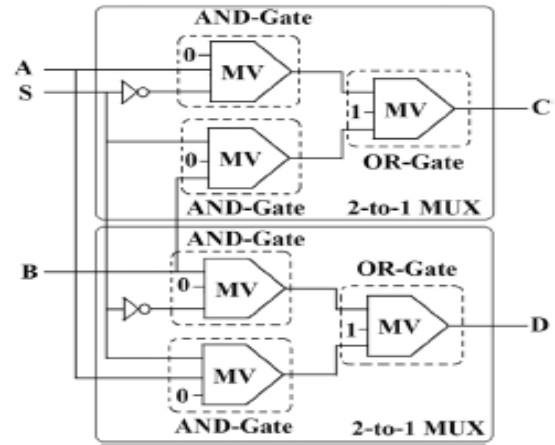


Fig. 3 2x2 Crossbar switch (Schematic diagram)

The design is made up of 6 Majority gates (MVs) and 2 inverters. The logic expression for 2x2 crossbar switch is given as

$$C=AS'+BS \quad (5)$$

$$D=AS+BS' \quad (6)$$

IV. CIRCUIT SWITCHING NETWORK

Circuit switching is a method of implementing a communication network in order to establish a connection between two nodes before they may communicate. It is shown in Fig. 4 An example for circuit switching network is the conventional telephone network. When a call is made by any user, it creates a connection between two telephones with the help of switches in telephone exchanges for as long as the call lasts.

Circuit switching network is made up of a set of switches and it is connected by physical links. In which each link is divided into n channels. The advantages of circuit switching network is that it provides the continuous transfer of data by using same path with the maximal utilization of available bandwidth for that communication. One disadvantage is that it is inefficient because the unused capacity to a dedicated connection cannot be used by a other connections on the same network.

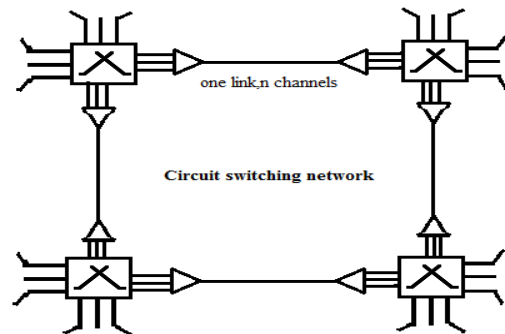


Fig. 4 Circuit switching network

V. EXISTING SYSTEM

A multiplexer is a device which allows one or more signals to be selected, combined and transmit on a single shared medium at a high speed. It is a multiple input and single output switch. With two input signal and one output signal, it is referred to as 2x1 Multiplexer.

In existing system, crossbar switch is composed of 2x1 MUX which uses 22 cells and of area $0.03\mu m^2$. QCA layout of existing 2x1 MUX is shown in Fig.5

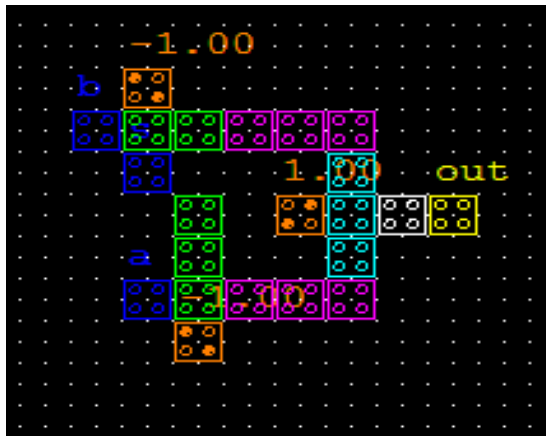


Fig.5 Existing 2x1 Multiplexer

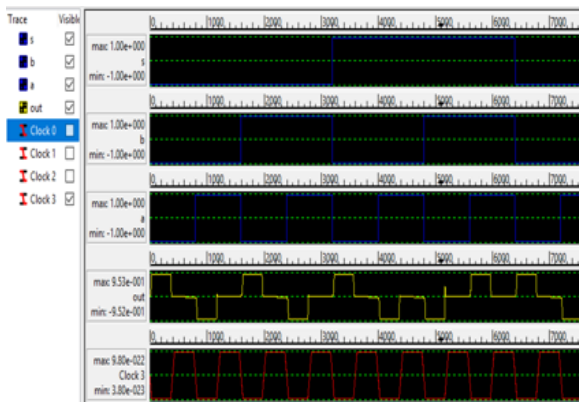


Fig. 6 Simulation result of existing 2x1 Multiplexer

If switch is zero (i.e.,S=0), input will be appeared in out.If switch is one (i.e.,S=1),input b will be appeared in out.The

simulated result of existing 2x1 Multiplexer is shown in Fig.6. Different colours are used to represent input cells,output cells and clocking zones.The green, purple, turquoise, purple,turquoise, white, blue, yellow, orange represents Clock 0, Clock 1, Clock 2, Clock 3,input cells,output cells and fixed polarisation. It has 3 Majority gates(MVs)If a cell is fixed as -1 in majority gate,it act as AND gate .If a cell is fixed as 1 in majority gate ,it act as OR gate.The existing 2x1 Multiplexer is composed of two AND gate and 1 OR gate.

Existing crossbar switch is composed of two 2x1 Multiplexer.The design uses 126 cells and of area $0.14\mu m^2$.The operation of 2x2 crossbar switch is explored in Table 1.QCA layout of existing crossbar switch is shown in Fig.7. The simulated result of existing crossbar switch is shown in Fig.8.

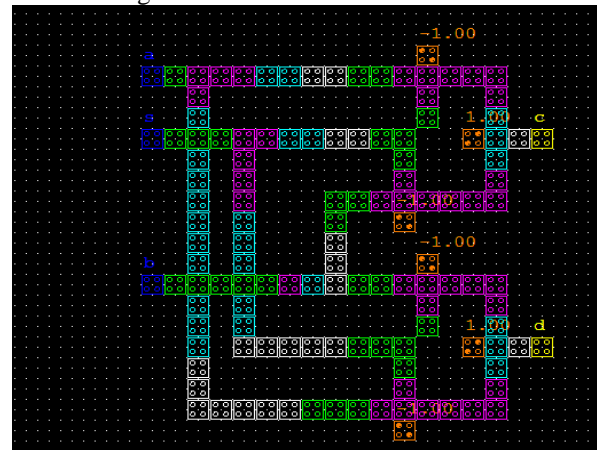


Fig.7 Existing 2x2 Crossbar switch

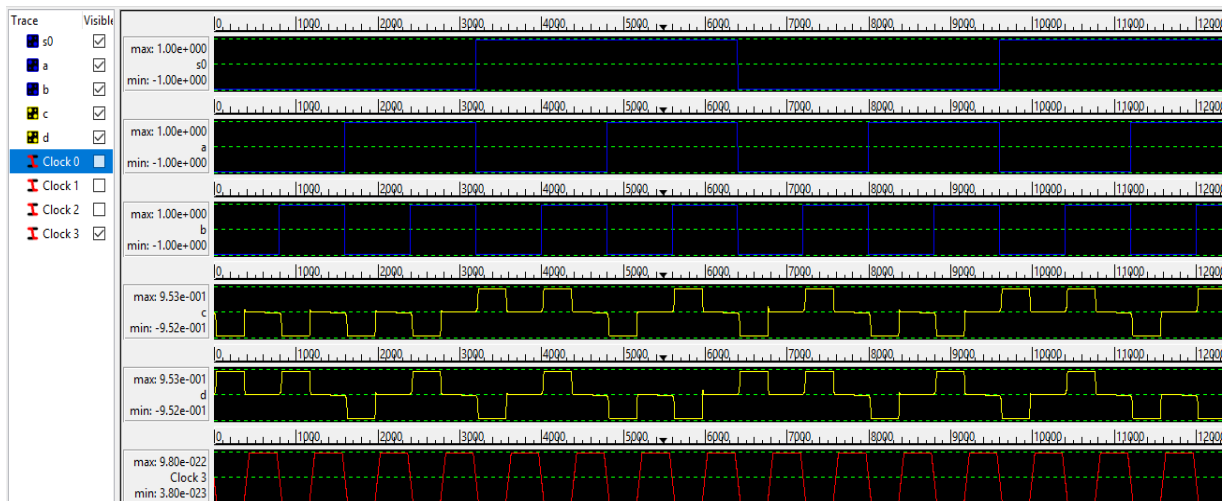


Fig.8 Simulation result of existing 2x2 crossbar switch

Existing transmitter block is composed of one 2x2 crossbar switch and one 2x1 multiplexer. It uses 178 cells and of area $0.24\mu m^2$.QCA layout of existing transmitter block is shown in Fig.9 Its simulation result is shown in Fig.10. 'a' and 'b' are the inputs to the transmitter, 'out' is the corresponding transmitter output.s1 and s2 are the control inputs.

If s1 = 0 and s2 = 0, the input given to 'a' will appear at output line 'out', i.e., out=A. If s1= 0 and s2= 1, the input

given to 'b' will appear at output line out, i.e., out=b. Similarly, by considering the different values of control bits s1 and s2 the truth table for transmitter block is formed and shown in Table II. Thus, the logic expression for output bit, out can be drawn as

$$\text{Out}=(S2)'[a(s1)'+b(s1)]+[b(s1)'+a(s1)] \quad (7)$$

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Table II: Truth table of transmitter block

Input		Output
Control bit (s1)	Control bit (s2)	Output (out)
0	0	a
0	1	b
1	0	b
1	1	a

Existing receiver block is composed of one 2x2 crossbar switch and one 1x2 demultiplexer. It uses 166 cells and of area $0.21 \mu m^2$. QCA layout of existing receiver block is shown in Fig.11 Its simulation result is shown in Fig.12. 'I' is the input to the receiver and 'c' and 'd' are the corresponding output to the receiver block. 's0' and 's' are the control signals. In these receiver block, if $s0=0$ and $s=0$, the input given to 'I' will appear at output line 'c', i.e., $c=I$. If $s0=0$ and $s=1$, the input given to 'I' will appear at output line 'd', i.e., $d=I$. The

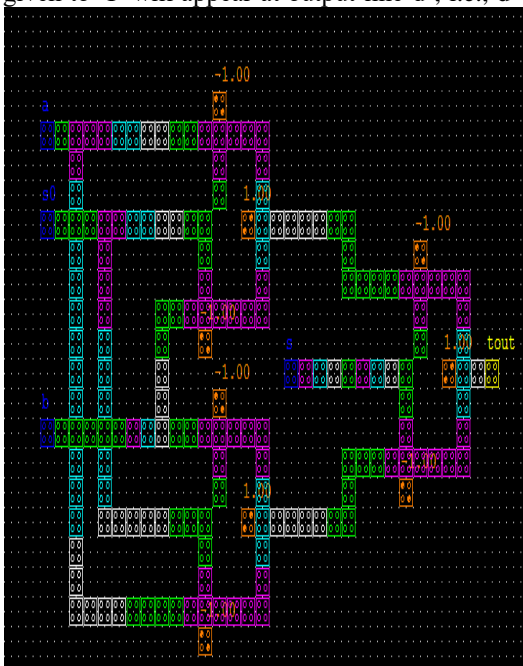


Fig 9. Existing Transmitter block

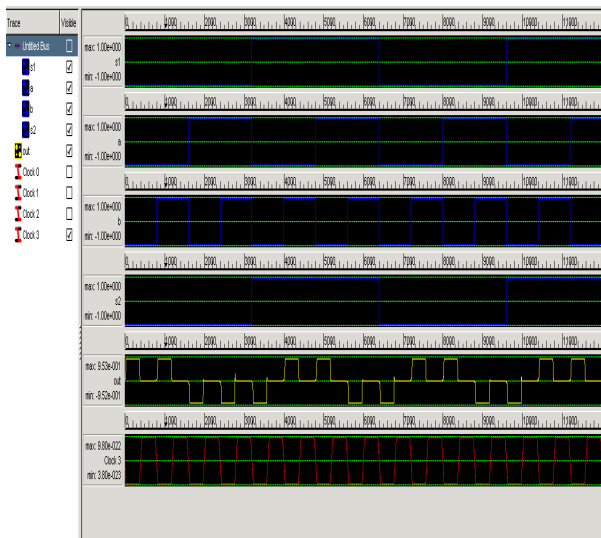


Fig.10 Simulated result of existing transmitter block

truth table for receiver block is formed and shown in Table III. Thus, the logic expression for output bits, c and d can be drawn as

$$c=(Is)'(s0)'+(Is)(s0) \quad (8)$$

$$d=(Is)(s0)'+(Is)'(s0) \quad (9)$$

Table III: Truth table of receiver block

Input		Output	
Control bit (s)	Control bit (s0)	c	d
0	0	I	0
0	1	0	I
1	0	0	I
1	1	I	0

Existing circuit switching network is composed of transmitter block and receiver block which uses 382 cells and of area $1.02 \mu m^2$. QCA

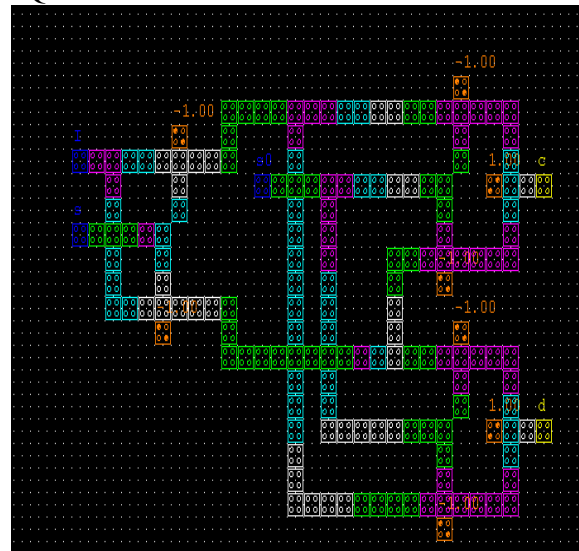


Fig.11 Existing receiver block

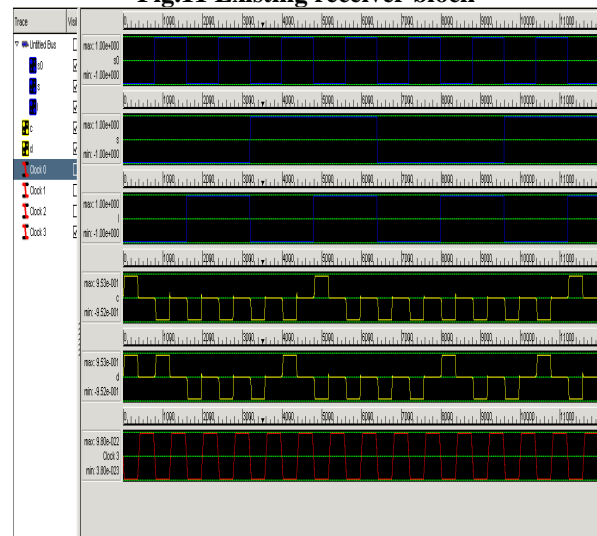


Fig. 12 Simulated result of existing receiver block

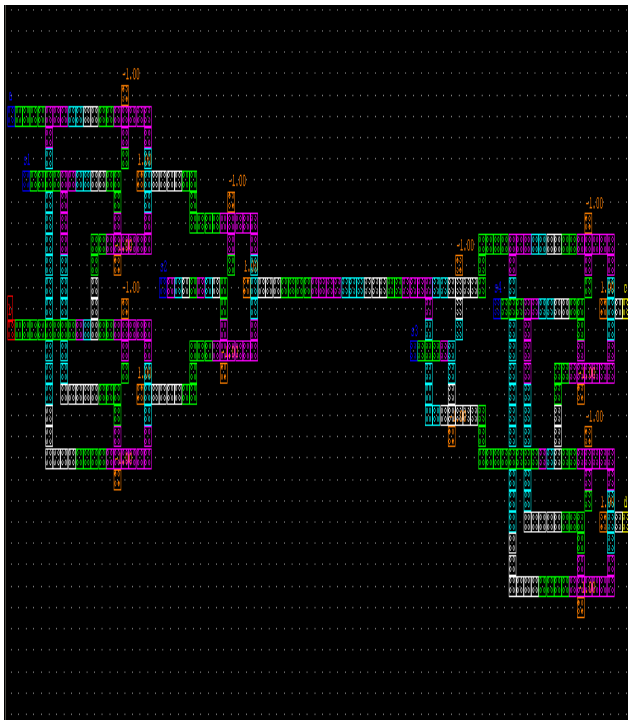


Fig.13 Existing Circuit switching network

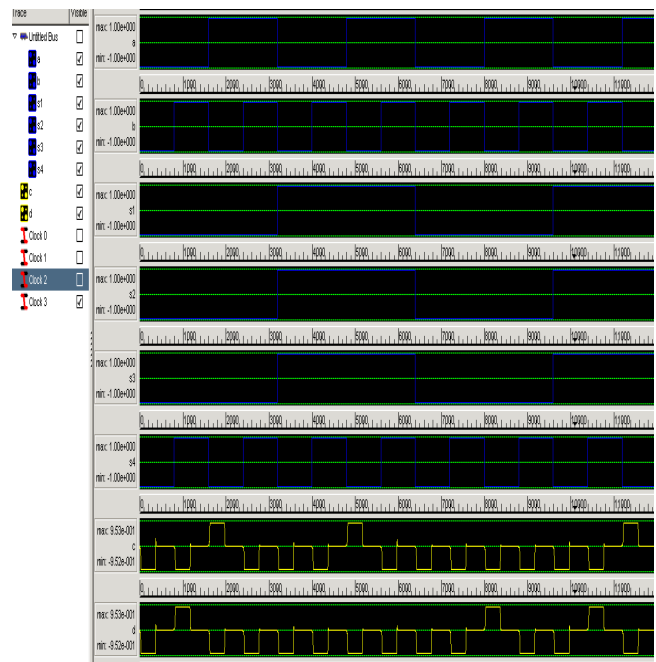


Fig.14 Simulated result of circuit switching network block is shown in Fig.19 and its simulation result is shown in Fig.20. Proposed receiver block is composed of one 2x2 crossbar switch and one 1x2 demultiplexer. It uses 136 cells and of area $0.16 \mu m^2$. QCA layout of proposed receiver block is shown in Fig.21 and its simulation result is shown in Fig.22

VI. PROPOSED SYSTEM

In proposed system, a crossbar switch is composed of 2x1 MUX which uses 19 cells and area of $0.02 \mu m^2$. The difference between the layout of multiplexer in the proposed when compared to existing is that fixed polarization of the AND gates are common. QCA Layout of proposed 2x1 multiplexer is shown in Fig.15.

The simulation result of proposed 2x1 multiplexer is same as the existing 2x1 multiplexer and the result is shown in Fig.16.



Fig.15 Proposed 2x1 Multiplexer

Proposed crossbar switch is composed of two 2x1 multiplexer. The design uses 81 cells and area of $0.08 \mu m^2$. QCA layout of proposed crossbar switch is shown in Fig.17. The simulated result of proposed crossbar switch is shown in Fig.18

Proposed transmitter block is composed of one 2x2 crossbar switch and one 2x1 multiplexer. It uses 126 cells and of area $0.16 \mu m^2$. QCA layout of proposed transmitter

Proposed circuit switching network is composed of transmitter block and receiver block which uses 269 cells and of area $0.43 \mu m^2$. QCA layout of proposed circuit switching network is shown in Fig.23 and its simulation output is shown in Fig.24.

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Fig.16 Simulation result for proposed 2x1 Multiplexer



Fig.17 QCA layout for Proposed crossbar switch

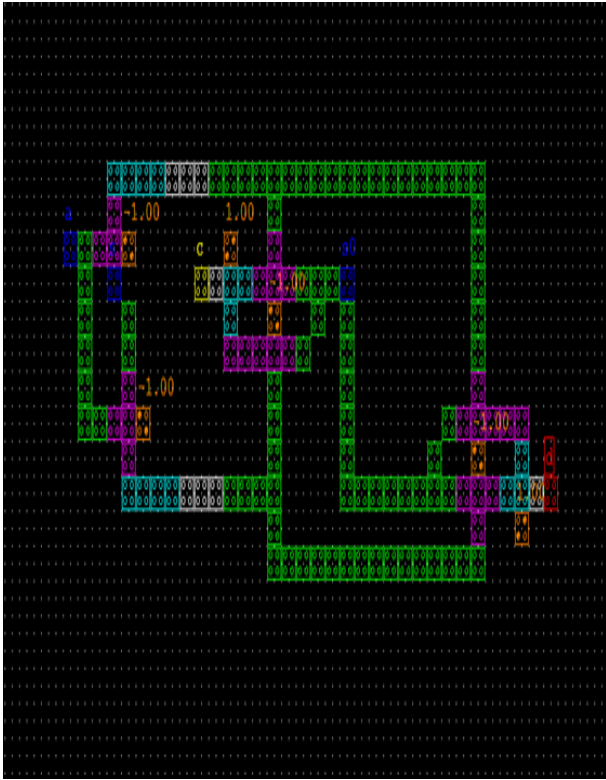


Fig. 21 QCA Layout for proposed receiver block

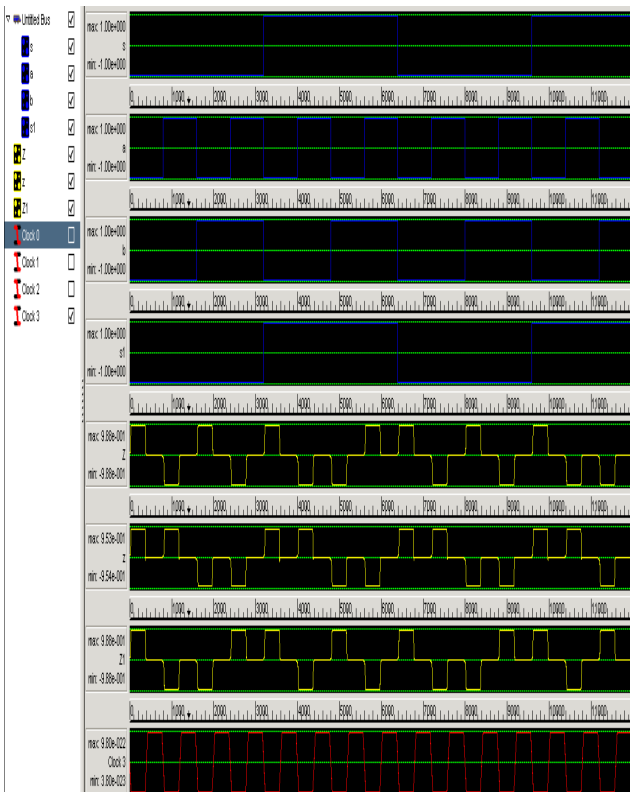


Fig.19. QCA Layout for proposed transmitter block

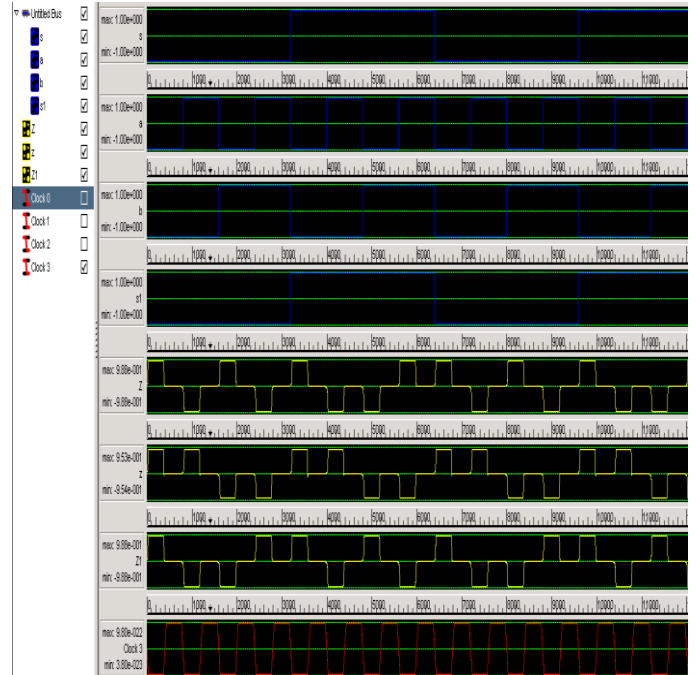


Fig.20 Simulation Output for proposed transmitter block

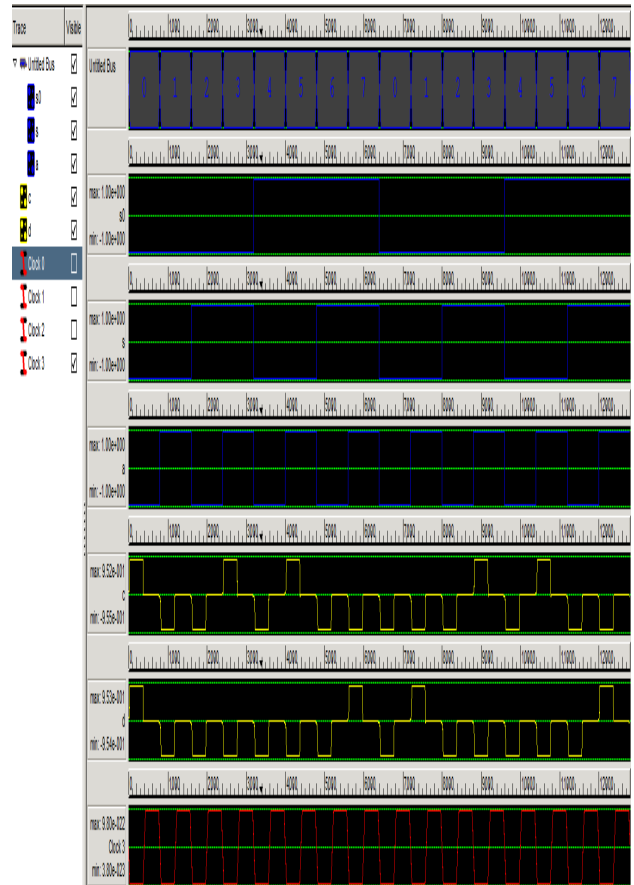


Fig.22 Simulation result for proposed receiver block

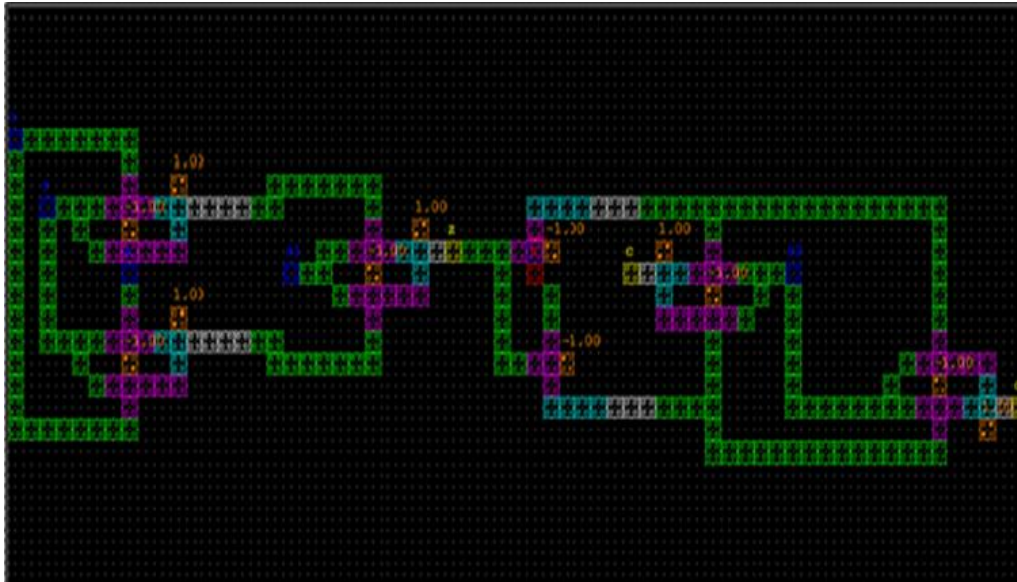


Fig.23 QCA Layout for proposed Circuit switched network

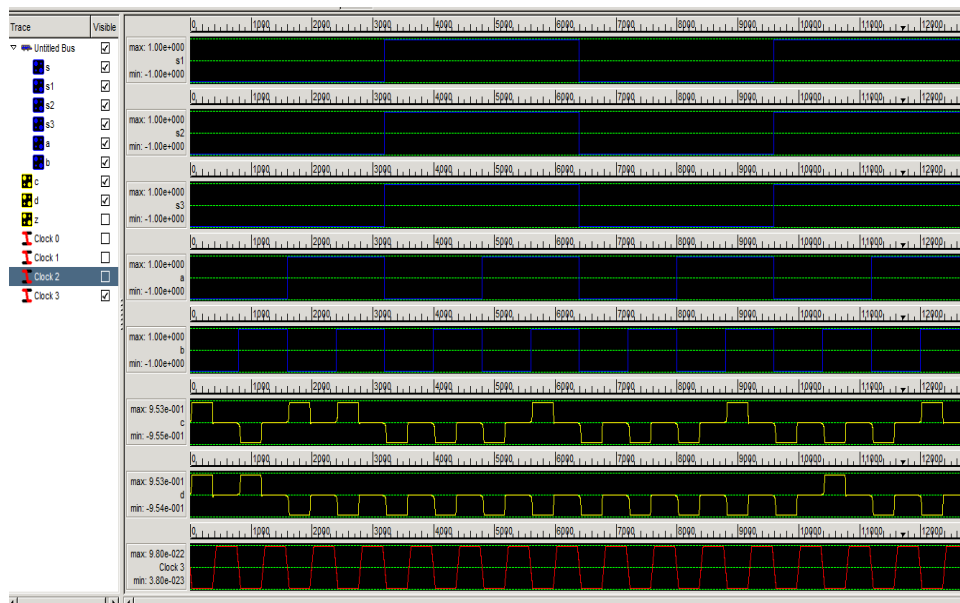


Fig.24 Simulation result for circuit switched network

Table IV: COMPARISON OF CELL COUNT

Design	Existing design	Proposed design
Crossbar switch	126	81
Transmitter	178	126
Receiver	166	136
Circuit switched network	382	269

Table V: COMPARISON OF AREA

Design	Existing design (μm^2)	Proposed design (μm^2)
Crossbar switch	0.14	0.08
Transmitter	0.24	0.16
Receiver	0.21	0.16
Circuit switched network	1.02	0.43

The table IV shows the comparison cell count result for Existing design and proposed design whereas table V shows the comparison of area for existing design and proposed design.

VII. CONCLUSION AND FUTURE SCOPE

In this paper, single layer circuit switched network in QCA technology is simulated. The design of circuit switched network is achieved using a crossbar switch, multiplexer and demultiplexer. Cell count and area are reduced when compared to the existing circuit switched network. In this paper, area and cell count of the proposed circuit switching network are reduced by 57.84% and 29.58% respectively when compared to existing circuit switching network. In future, Circuit Switching Network can be implemented with multiple users using mxn crossbar switch.



REFERENCES

1. Jadav Chandra Das, Debashis De “Circuit switching with Quantum - Dot Cellular Automata” Nano Communication.Net (2017).
2. Mazaher naji, saeed Rasouli, A unique structure for the multiplexer in quantum-dot cellular automata, 2017.
3. Hamid Rashidi, Abdalhossein Rezai, High-performance multiplexer architecture for quantum-dot cellular automata, 2016.
4. J.C. Das, D. Debashis, Nanocommunication network design using QCA reversible crossbar switch, Nano Communication Network (2017) nancom.2017.06.003.
5. D. Silva, L. Sardinha, M. Vieira, L. Vieira, O. Vilela Neto, Robust serial nanocommunication with QCA, IEEE Trans. Nanotechnol. 13 (3) (2015) 464–472.
6. J.C. Das, D. Debashis, User authentication based on quantum-dot cellular automata using reversible logic for secure nanocommunication, Arab J. Sci. Eng. 41 (3) (2016) 773–784.
7. J.C. Das, D. De, Quantum dot-cellular automata based reversible low power parity generator and parity checker design for nanocommunication, Front. Inf. Technol. Electron. Eng. 17 (3) (2016) 224–236
8. B. Sen, M. Dutta, M. Goswami, B.K. Sikdar, Modular Design of testable reversible ALU by QCA multiplexer with increase in programmability, Microelectron. J. 45 (2014) 1522–1532.
9. J.C. Das, D. De, Quantum dot cellular automata based cipher text design for nano communication, in: Proc. ICRCC, SKP Engg. College, Tamilnadu, India, 2012, pp. 343–348
10. J.C. Das, D. De, Nanocommunication using QCA: A data path selector cum router for efficient channel utilization, in: Proc. ICRCC, SKP Engg. College, Tamilnadu, India, 2012, pp. 43–47
11. N.G. Anderson, F. Maalouli, J. Mestancik, Quantifying the computational efficacy of nanocomputing channels, Nano Commun. Netw. 3 (3) (2012) 139–150
12. F. Yao, M.S. Zein-Sabatto, G. Shao, M. Bodruzzaman, M. Malkani, Nanosensor data processor in quantum-dot cellular automata, Nanotechnol. (2014).
13. L.H. Sardinha, A.M.M. Costa, O.P.V. Neto, L.F.M. Vieira, M.A.M. Vieira, Nanorouter: a quantum-dot cellular automata design, IEEE J. Sel. Areas Commun. 31 (12) (2013) 825–834.
14. Aruna, S., & Senthil Kumar. K. . Design of area-delay efficient adder based circuits in quantum dot cellular automata. International Journal on Intelligent Electronics Systems, 9(2), 1–8 (2015).
15. A.Chandrasekaran, K.Senthil kumar, Design of an Efficient Full adder Based on 5- input Majority gate in Coplanar Quantum dot cellular automata, Journal of Power Electronics and Devices, Volume 3, Issue 2(2017).
16. P.Pavithra, A.Chandrasekaran, K.Senthil kumar, An optimal design of odd parity generator by using five input Majority gate in Quantum dot Cellular Automata, Journal of VLSI Design and its Advancement, Volume 1, Issue 1(2018).
17. A. Chandrasekaran, K. Senthil Kumar, K. Hemalatha, “Design and Evaluation of a Majority Gate Based Encoder in Quantum-dot Cellular Automata (QCA)”, Journal of Electronics and Communication systems, volume 4, Issue 1(2019).



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