

Computational Smart Grid and DAM Queue



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Abstract: The layout of the Smart Computational Grid is an integral part of our information services infrastructure. In this paper, an anticipation of the architectural model of Smart Computational Grid is derived, which provides petabytes storage, tera-byte processing speed, and Gigabyte data transmission, where many supercomputers forms node with parallel data transmission. Impulsive demands posed by unexpected flows of data from users for complex computations, however, is exceedingly challenging. The paper specifies the design and implementation of a Dynamic Allocation of Memory (DAM) algorithm. DAM algorithm identifies the design of memory at supercomputing node, for ergodic functioning and for ensuring that no data overflow, using the methodology of queuing theory. The overall performance of the DAM algorithm is shown using MM1 and GG1 queue models and results are compared accordingly.

Keywords: Smart Computational Grid, DAM algorithm, MM1 queue, MG1 queue, Ergodicity.

I. INTRODUCTION

Smart Grid computing is an extension of distributed computing where the distributed resources can be shared and used effectively [1]. Nodes and resources are dynamically added in the Computational grid [2], wherein, the requests from these nodes are forwarded to super computer for processing. These super computers have immense processing capacity due to combination of distributed computing. All the requests from nodes stay in memory and waits for its turn to be processed. It was perceived during the experimentation that the requests need to be classified based on grain size[3]. The processing of the requests need to follow parallel processing [4], where the proposed DAM (Dynamic Allocation of Memory) algorithm categorizes the requests into three classes, C1, C2 and C3, and then each queue is processed in parallel. The classification is defined as[5]:

1. Fine Grain: Request to be takes less than 5 seconds
 2. Medium Grain: Request to be takes between 5 -10 seconds
 3. Coarse Grain: Request to be takes more than 10 seconds
- DAM algorithm is mentioned in Figure 1 mentioned below:

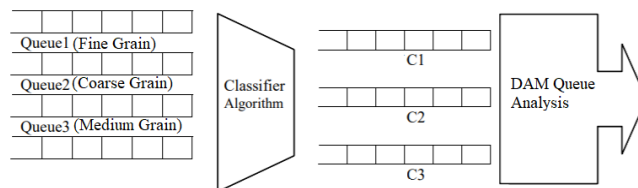


Fig 1: Layout and Interfacing of Computational Grid
We have merged all the queue classification into one queue, and conducted experimentation for DAM queue using both MM1 and MG1 queues. In the notation of queue, M is Markovian distribution and G is General distribution. For General distribution, we have choice between different types of algorithms – Bernoulli, Gaussian, Equiprobable, Negative-Exponential, Modified Geometric distribution. For the experiment, Bernoulli algorithm is taken for general distribution.

The paper is divided as follows: Section 2 gives the architecture of Smart Grid, which includes combination of super computer and nodes. Section 3 mentions the derivation of mathematical equations for DAM queue using MM1 and MG1 queue models respectively. Section 4 shows the outcomes based on mathematical derivations. Section 5 concludes the exploration with recommendations and further research.

II. ARCHITECTURE OF SMART COMPUTATIONAL GRID

In the anticipated study, the super computers form the node of Computational grid. The branches (*edge*) between two nodes are constructed using optical fibers having at least 144 cores. A dedicated cable between two nodes forms the computational grid. The layout is shown in Figure 2 below.

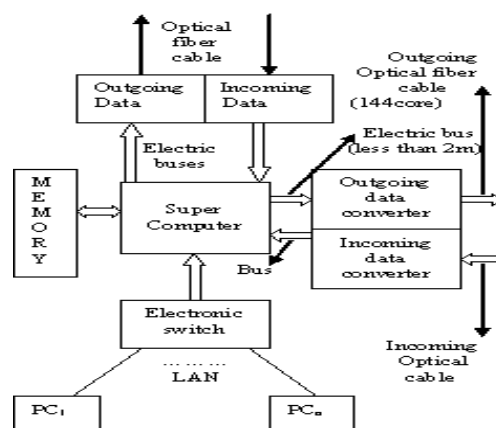


Fig 2: Layout and Interfacing of Smart Computational Grid

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It is apparent from Figure 1, that two sets of data converters will be obligatory for the computational grid node. They should be associated to the parallel port of the super computer [6]. The electric pulses are used for Incoming and Outgoing of data to the node, wherein, these pulses are transmitted in parallel which enables the grid to make super computer to process in parallel. The biggest challenge in designing of computational grid is of memory associated with super computer [7]. With such a bursty traffic arriving at a time, the memory design should be robust to manage the outflow and inflow of data [8]. Furthermore, the size of memory should be dynamic [9], so none of the data used for computations to be lost.

III. MEMORY DESIGN FOR SMART COMPUTATIONAL GRID

Memory Design is an imperative ingredient of design of Smart Computational Grid. For λ requests arriving at the system and μ responses departing, the computational grid should presume that the functioning is stable, when $\lambda < \mu$. Such a condition is called Ergodicity [10], which defines the condition such that there is no overflow of data.

A. Derivation of Queue Length using MM1 model for DAM queue

For estimation of the Queue Length using MM1 model, following conditions are assumed:

1. Δt is value in which only one process can occur, i.e., either arrival or departure of request/response.
2. State of arrival is denoted as λ and state of departure is denoted as μ .

Probability of 1 arrival = $\lambda \Delta t$

and, the probability of 1 departure = $\mu \Delta t$

Then, probability of 0 arrival = $1 - \lambda \Delta t$

and, the probability of 0 departure = $1 - \mu \Delta t$

Considering n data to present at any time t and is denoted by $P_n(t)$. If the time is increased from t to $t + \Delta t$, then

$$P_n(t + \Delta t) = \begin{cases} P_n(t)(1 - \lambda \Delta t)(1 - \mu \Delta t) \\ P_{n+1}(t)(\mu \Delta t) \\ P_{n-1}(t)(\lambda \Delta t) \end{cases} \quad (1)$$

Upon solving eq. (1), we get

$$P_{n-1}(t)\lambda - (\lambda + \mu)P_n(t) + P_{n+1}(t)\mu = 0 \quad (2)$$

Assuming there were 0 requests at time $(t + \Delta t)$, eq. (2) can be stated as:

$$P_0(t + \Delta t) = P_0(t)(1 - \lambda \Delta t)$$

$$= P_1(t)\mu \Delta t$$

$$= P_0(t)(1 - \lambda \Delta t) + P_1(t)(\mu \Delta t)$$

$$\left(\frac{P_n(t + \Delta t) - P_n(t)}{\Delta t} \right) = P_n(t + \Delta t) = -P_0(t)\lambda + P_1(t)\mu \quad (3)$$

For stable condition, eq. (3) is set to 0, we get the following

$$P_1(t) = \left(\frac{\lambda}{\mu} \right) P_0(t) \quad (4)$$

Generalized equation becomes

$$P_n(t) = \left(\frac{\lambda}{\mu} \right)^n \left(1 - \frac{\lambda}{\mu} \right) \quad (5)$$

Queue Length can be thus mentioned as

$$Q_L(MM1) = \sum_{n=0}^{\infty} n P_n(t)$$

$$= \sum_{n=0}^{\infty} n \left(\frac{\lambda}{\mu} \right)^n \left(1 - \frac{\lambda}{\mu} \right)$$

$$= \left(1 - \frac{\lambda}{\mu} \right) \sum_{n=0}^{\infty} n \left(\frac{\lambda}{\mu} \right)^n$$

$$Q_L(MM1) = \frac{\left(\frac{\lambda}{\mu} \right)}{\left(1 - \frac{\lambda}{\mu} \right)} \quad (6)$$

Eq. (6) can be unswervingly applied to estimate the value of queue length. Also, size of queue should also include Standard deviation for overall derivation of queue length [11], as given below:

$$\hat{M} = \text{Average Queue Length} + \text{Standard Deviation}$$

It was also observed that Standard Deviation is same as Average Queue Length, thus, overall queue length is mentioned as

$$\hat{M} = 2 \cdot \left(\frac{\frac{\lambda}{\mu}}{1 - \frac{\lambda}{\mu}} \right) \quad (7)$$

B. Derivation of Queue Length using MG1 model for DAM queue

It was observed during study that MG1 model follows second order polynomial equation [12] as mentioned in eq. (8) below

$$Q_L(MG1) = y_i = a_0 x_i^2 + a_1 x_i + a_2 \quad (8)$$

where a_0 , a_1 and a_2 are coefficients of polynomial for MG1 model.

Eq. (8) gives the queue length for larger values of data and not for lower values.

There are two challenges while deriving the queue length for MG1 model:

1) Finding the values of coefficients a_0 , a_1 and a_2

2) Identifying the value of $x(i)$

Values of a_0 , a_1 and a_2

To calculate the coefficient values, "S" represents the error in computation and can be mathematically represented as

$$S = \sum (y_i - \hat{y}_i^2) = \sum (y_i - a_0 x_i^2 - a_1 x_i - a_2)^2 \quad (9)$$

If be differentiate S w.r.t a_0, a_1, a_2 and setting each of these coefficients equal to zero, we get

$$\left. \begin{aligned} n a_0 + a_1 \sum x_i + a_2 \sum x_i^2 &= \sum y_i \\ a_0 \sum x_i + a_1 \sum x_i^2 + a_2 \sum x_i^3 &= \sum x_i y_i \\ a_0 \sum x_i^2 + a_1 \sum x_i^3 + a_2 \sum x_i^4 &= \sum x_i^2 y_i \end{aligned} \right\} \quad (10)$$

where "n" is the degree of polynomials, which can be solved using Gauss Elimination method.

Table 1 shows the summations of variable rate of arrivals, $Q_L(MG1)$, their products and powers.

Table- I: Variables of M/G/1 model

$x_{n=0}$	5000	7500	10000	12500	15000	$\sum x_i = 50000$
y_i	5102	7653	10204	12755	15306	$\sum y_i = Q_i = 51020$
x_i^2	2500000	5625000	10000000	15625000	22500000	$\sum x_i^2 = 56250000$
x_i^3	12500000	4218750000	10000000000	19531250000	33750000000	$\sum x_i^3 = 68750000000$
x_i^4	6250000000	3164062500000	10000000000000	24414062500000	50625000000000	$\sum x_i^4 = 88812500000000$
$x_i y_i$	25510000	57397500	102040000	159437500	229590000	$\sum x_i y_i = 573975000$
$x_i^2 y_i$	12755000000	43048125000	102040000000	199296875000	344385000000	$\sum x_i^2 y_i = 705200000000$

When the last column of the table is substituted in normal equation, we get the following three simultaneous equations for variables, a_0 , a_1 and a_2 .

Normal equations are:



$$5a_0 + 50000a_1 + 562500000a_2 = 51020$$

$$50000a_0 + 526500000a_1 + 687500000000a_2 = 573975000$$

$$526500000a_0 + 687500000000a_1 + 8882812500000000a_2 = 7015250000000$$

By Gaussian elimination and using back substitution, we obtain:

$$a_0 = 1.0089 \times 10^{-7}$$

$$a_1 = 1.001$$

$$a_2 = 1.68 \times 10^{-6}$$

Value of x(i)

To identify the value of x(i), we use Bernoulli algorithm as mentioned below:

```
begin
read λ, n
b=1/λ
a=1-b
for i=1 to n in step of one do
q(i)=RAND U(i)
x(i)=(-a+SQRT(a*a+2*b*q(i)))/b
write(x(i))
end for
end
```

IV. OUTCOMES

The outcomes of the research conducted is mentioned in Figure 3, which clearly indicates that for the given number of requests arriving, MG1 model superseded MM1 model. The result evaluates the queue length, which indirectly identifies the memory size of the supercomputing node at the Computation Grid (as mentioned in Figure 2). However, the following conditions were followed to observe the outcomes:

1. Arrival should be less than a threshold value (predecided < μ).
2. In general, if memory is designed as derived in Eqs. (7) and (8), there will be no overflow of data.
3. All the class of requests (C1, C2, and C3) were merged into one queue to get the queue length.
4. If the size of request is large, then host computer assigns the module for the program, to rest of the other supercomputers. The other supercomputers after processing the program module return the program back to the host computer, which in turn, sums up the result give the desired output.

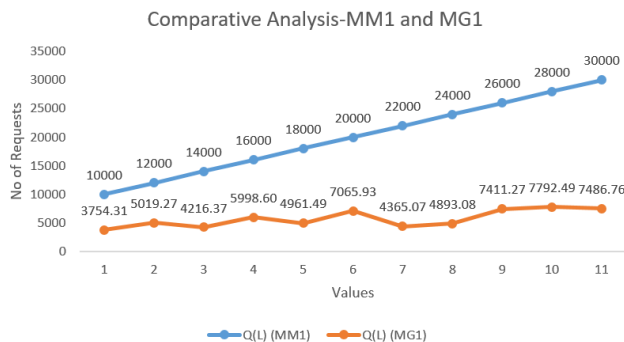


Fig 3: Outcomes based on MG1 and MM1 queue

V. CONCLUSION

Before y The outcome gives the clear indication that MG1 model should be adopted for query processing for Computational Smart Grid. As mentioned in the outcome, the

queue length (or memory size) required will be almost half, if we use MG1 model, or in other words, Computational grid can be used to process complex computations fast. But, these outcomes are based on certain predefined conditions, and moreover, other queue models are also to be tested for the same number of requests. DAM queue can also use GM1 and GG1 queue models, which will be used and compared for future research.

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