# Positive Output Elementary Super Lift Luo Converter Using Pi Controller

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Abstract: - An Electron device and circuits that converts an origin of direct current from one particular level of voltage to another is represent a boost converter. To achieve high step up voltage gain, it uses coupled inductor and the voltage lift technique. However, there is a reduced stress on the animated switch as it is attached and the energy deposited in the leakage conductor is reused, hence increasing its conversion efficiency. In this paper, a Proportional-Integral control has been designed for the positive output of the Elementary Super Lift LUO Converter using a bidirectional Capacitor, Super Lift LUO converter which uses the voltage lift capability to donate a less ripple, very high power density, higher efficiency, voltage gain and a less complicated structure. A comprehensive analysis and modeling was also implemented.

Keywords: LUO converter, switched capacitance, phase shifters, micro fabrication, inverter.

#### I. INTRODUCTION

DC to DC changes is an application that has been expanding rapidly and these are also generally used in industries as well. The positive output Luo converter is another series of DC to DC converter as it has high power density and efficiency. It is used in many industrial applications, Switched Mode Power Supply, etc. The block diagram for the PI Control has been given below.



Figure 1. Proposed method block diagram

The proposed method considerably enlarge the Voltage Transfer Gain. However these circuits are complex in nature [1-2]. The static and dynamic behavior of the power converters becomes nonlinear due to the time variations and its switching nature. A good control over these converters ensures stability in arbitrary operation condition. The PI control method offers lot of advantages over the Proportional

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derivative and integral control such as:-stability, robustness, good dynamic response and many more [3]. In this paper, a PI (Proportional- Integral) control has been designed for the positive output of the Elementary Super Lift LUO Converter [4-5].

## **II. MATERIALS AND METHODS**

Switched capacitor- This is a circuit element preowned for processing of discrete time signal. When the switches are opened and closed, the charges move in and out of the capacitor [6]. The signals to the switches are usually controlled by non-overlapping signals. Since these filters based only on the ratios between the capacitances, they are more suitable for use within integrated circuits [7]. Switched capacitor resistor- This is the simplest circuit made of one capacitor and two switches. The combination of these three behaves like a resistor whose value depends on capacitors and frequency with the added benefit of adjusting the Value with a change in the frequency [8].



Figure 2. The parasitic sensitive integrator

This circuit is used to provide accurate voltage gain as well as performs the functions of an integrator shown in figure 2.



Figure 3. Discrete time system



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A discrete time system processes as shown in figure 3. It is a process based on micro machining. The process of fabrication matches that of CMOS fabrication. Antennas-Components like RF MEMS switches and reactors so as to take advantage of low insertion loss and high Q factor. The range of frequencies can go from 2.4 to 18 GHz. Filters-These filters can be used in case the antennas failed. The size of the filter decreases when it is a Tunable RF band pass filter. Phase shifters- The applications of these devises include controlling the elative phase of each element in a phase array antennae. TTD (True Time Delay) phase shifter allows UWB radar waveforms with high range resolution [9-10]. 2 distinct conditions are chosen when the converter operates in the motoring mode, if the power gradient on the load side is between -3500 watts to +3500 watts, battery modules supply energy to the load otherwise UC modules supply power to the load [11-12].



Figure.5. Four quadrants operations of in the LUO converter



**III. SIMULATION RESULTS AND DISCUSSIONS** 

d Cap Bar 120 Ca

Figure 6. Simulation diagram of proposed method The distributed load line principle or switched line principle is used in designing TDD phase shifters. Electrical load- Any device that is connected to a power line and derives power is called a load. Load affects the performance of the power line.

DC motor- A motor is any device that converts electrical power to mechanical power and is divided into many types. First being the brushed motor. This motor generates torque as it is directly connected to the power line. It works on the principle of Lorentz force. The advantages of this motor are reliability and low initial cost. Disadvantages include high maintenance and low life span. The other type is the synchronous motor. The commutation is external in this motor when compared to that of the brushed motor which is internal. The last type is the brushless motor which uses a permanent magnet in the rotor. Advantages include long life span and low maintenance whereas the disadvantages include high initial cost and complicated controls.

Controller implementation - As the controller directs the rotations of the rotor, it needs some sort of orientation determiner of the rotor. The DC output of the controller appears to be trapezoidal. The controllers are equipped with three bi directional drivers to drive the high DC power. The BLDC motors dominate many applications of today ranging from CD/DVD players to cooling fans. These can be constructed in several different physical configurations. In all BLDC motors all coils are stationary.





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Figure 8. Diagram for LUO converter



Figure 9. Circuit diagram for inverter

This paper proposes a new controller design for a 2-quadrant SC LUO converter for high voltage energy storage and energy management applications.

To design this device, the following parameters were specified:-

f0A= 529 Hz, QA=0.785 f0B=993 Hz, QB=3.559 for unity gain at DC, H0A=1, H0B=1

A stage with lower Q is placed ahead of a higher Q as this will provide attenuation at the center frequency. The simulation of switched capacitance, LUO converter, inverter shown in figure 7, 8 and 9 respectively.



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The output capacitor voltage are illustrated in figure 10. The total harmonics distortion of this proposed method is 16 are demonstrated in figure 11. The output voltage of inverter after filter with amplitude 270V shown in figure 12.



Figure 11. Total harmonics distortion



Figure 12.Output voltage of inverter after filter



Figure 13. Performance of motor



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The DC bus voltage, torque, speed and stator current of motor are illustrated in figure 13.at the starting the amplitude of DC bus voltage if high then reduced to 500V. The reverence speed and actual speed of the motor are matched from 0 seconds to 1.5 seconds from 1.5 seconds to 1.8 seconds there is a slight deviation after that speed is again matched with reference speed.

	Foergy stumps	CODES	MOS	MOS <sub>2</sub>	MOS	MO54	MOSt	MOS
2	Ulm sapacitor	1	UN	OFF	05	UN	OFF	OFF
		2	ON	OFF.	OFF	ON	086	OFF
			OFF	- CPK	Chi	10%	OFF	OFF
		4	OFF	ON	OFF	ONC	ON	OFF
	Battery	0	OFF	OFF	OFF	OFF	OFF	OFF
	Ultra- capacitor	5.	OFF	105	OFF	105	ON .	OFF
4		0.	OFF	ON	ON .	000	OFF	OFF
â.		- T-	ON:	OFF	OLL	ONC:	OFF	(1)
	Battery	0	097	OFF	OFF	OFF	OFF	OFF

**Table 1.Switched codes** 

### **IV. CONCLUSION**

This proposed design was completed successfully with the advantages like Good regulation capability. Ease of speed capacitor control and balanced voltage. А Proportional-Integral control has been designed for the positive output of the Elementary Super Lift LUO Converter which uses the voltage lift capability to donate a less ripple, very high power density, higher efficiency, voltage gain and a less complicated structure.

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