Design of 3-Bit Digital RF Phase Shifter for Wireless Communication

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Abstract: In this paper, a 3-bit digital phase shifter based on switched transmission line technique using coplanar waveguide is proposed. The design has the resonant frequency of 10 GHz which can be used in wireless communication applications. Recent developments in radio frequency components development has raised as a significant way for constructing low loss phase shifters. MEMS phase shifters whose insertion loss is low and high isolation uses minimum power. This helps to bring low cost and light weighted phased array antennas. The transmission line length and wavelength decides the characteristics of phase shift. The phase shifter design consists of coplanar waveguides having center conductor width of 100µm and the gap of 14 µm on a FR-4 epoxy substrate with thickness of 1.6mm. The design is simulated using ADS to yield phase shift of 45°, 90° and 180°. The benefits of digital phase shifters include achieving flat phase over a wide bandwidth as well as having higher power handling and linearity with uniform performance. The phase shifters are used in different fields which includes microwave devices, feeder of radio system, phased array antenna, coherent radio system, etc.

Index Terms: Radio Frequency, Microelectromechanical Systems, Coplanar Waveguide, phase shifter.

I. INTRODUCTION

Phase shifters are used in RF and microwave communication, radar and measurement systems and also benefits applications in phased array antenna and radio communication. In a RC network, the output can lead or lag the incoming signal by 90° in a phase shifter. Analog and digital phase shifters are the different kinds of phase shifters. Analog phase shifters are controlled by voltage level and it is varied continuously. The analog type of phase shifters are usually narrowband and its wide frequency ranges upto 10 GHz. The type of phase shifters used here is digital phase shifters. Variation of time delay or discrete phase shift is given by digital phase shifter. More number of digital phase shifters are used which is an essential component in satellite systems. The losses and noises can be eliminated by using digital phase shifters. Based on PIN diode assembly, the digital phase shifters are modeled. The phase angle of a RF signal is changed by a passive microwave element, RF phase shifters. Loaded line phase shifter and switched line phase shifter are different kinds of MEMS phase shifters. A transmission line loaded with two different impedances values selected by two switches is loaded line phase shifter whereas by using switches, the input signal is switched between lines of different electrical lengths to get different phase shifts is switched line phase shifters. It has less power consumption than loaded line. The switched line phase shifter is one of the easiest ways to implement the design and is fabricated on PCB by its own.

II. TRANSMISSION LINE

An electrical signal transmitted over a pair of electrical conductors from one range to another is referred to as transmission line. In order to transmit high radio frequency (RF) signals and acoustic signals, transmission lines are used. Planar transmission lines are the most commonly used line at RF range. Planar lines are conductors or strip lines which is flat and ribbon shaped. They are interconnectors used on printed circuits and integrated circuits due to its fitting in the manufacturing units. They are also called as dielectric strips. The types of planar transmission lines most commonly used at RF range are strip line, microstrip line and coplanar waveguide. The first ever found transmission line is strip lines which have strip conductor centered between two parallel ground planes with two equal slabs of a dielectric, ferrite or semiconductor medium separating the centre conductor from the ground plane. The medium used here is solid material and the air is used as a actual dielectric. It has good electromagnetic shielding and low attenuation loss which is suitable for high quality factor and low interference applications. Microstrip lines, shwon in figure 1 have single conductor trace on one side and single ground plane on the other side. It is open structure and it can be easy for fabricated and though it offers low electrical performance, attenuation loss and power handling are compromised.

\[
\epsilon_0
\]

\[
\epsilon_r
\]

(a)
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The electromagnetic waveguides are carried by coplanar waveguide (CPW) both exist partly in dielectric substrate and air. The center conductor and ground planes are situated on the substrate surface. The transmission line which is mostly suitable for high frequency applications is CPW. Coplanar waveguides are uniplanar in solid state quantum. The advantages that includes in CPW are dispersion is low, realization of short circuit ends are very simple, easy for attaching shunt and series circuit elements and undesirable parasitic inductances are introduced, since there is no need for via holes and also the performances are limited at high frequencies. CPW promotes a quasi-TEM dominant. Nowadays, high power electronic applications can be operated at frequencies of 100 GHz and beyond the frequency ranges. At such frequencies, for device are connected and signal is distributed through transmission lines. Coplanar waveguide are used for fabricating thick substrates has it has supporting wave propagation is located on the same plane. For signal characterization and MMICs coplanar waveguide has been used.

The thickness of 1.6mm, a dielectric constant of 4.4 and loss tangent is 0.002. The characteristic impedance of the coplanar transmission line is 50 Ω. The thickness of the copper conductor material is given as 17 µm. The design aims to provide better and accurate phase shifts with very low insertion loss and better return loss for the 3 different phase shifts. The guided wavelength of the transmission line is given by

\[ \lambda_g = \frac{\lambda}{\sqrt{\varepsilon_{\text{eff}}}} \]  

(1)

where \( \lambda \) is the free space wavelength given as \( c/f \) where \( c \) is the free space velocity (3 x 10^8 m/s) and \( f \) is the operating frequency, \( \varepsilon_{\text{eff}} \) is the effective dielectric constant which is given by

\[ \varepsilon_{\text{eff}} \approx \frac{\varepsilon_r + 1}{2} \]  

(2)

The 3-bit phase shifts intended for this paper has the line lengths of design of 2304 µm for a 45° phase shift, 4300 µm for 90° phase shift and 9000 µm for 180° phase shift of a CPW transmission line center conductor width (W) is 100 µm and the gap (G) is 14 µm fabricated on a FR-4 substrate. The layouts of each bit phase shifter representing 45°, 90° and 180° are shown in figure 3, 4 and 5 respectively.

III. PHASE SHIFTER DESIGN

Switched transmission line technique is used for designing phase shifters in this paper. The phase shifters of different degrees such as 45°, 90° and 180° are designed at 10 GHz operating frequency which is useful for wireless communication. The design consists of FR-4 substrate with
IV. SIMULATION RESULTS

The design of various bit phase shifters are simulated using Advanced Design System (ADS) software. The change in phase shift and best quality performance than the conventional RF phase shifter in terms of low insertion loss are due to the characteristic impedance change and the structure change. The simulated results of 45°, 90° and 180° phase shifters using ADS software is shown in figure 6 -14. The digital phase shifter designed at 10 GHz operating frequency provides better return loss (S11 dB) for 45° phase shifter design is -21.864 dB Shown in figure 6 and insertion loss (S12 dB) below -0.08 dB as shown in figure7. Figure 8 shows the phase shift (S12 phase) of 45.55° at 10GHz which agrees with the required design of 45°. The 90° phase shifter design provides return loss (S11 dB) of -18.41 dB and insertion loss (S12 dB) below -0.167 dB as shown in figure 9 and 10. The phase shift obtained for 90° bit design which is 84.76° which is shown in figure 11 with a phase error of 2.89°. The 180° digital phase shifter design provides better return loss (S11 dB) of –36.56 dB and insertion loss (S12 dB) below -0.22 dB as shown in figure 12 and 13. The phase shift obtained for 180° bit which is 177.10° which is shown in figure 14 with a phase error of 2.89°.

Figure 6. Return loss for 45° design

Figure 7. Insertion loss for 45° design

Figure 8. Phase shift for 45° design

Figure 9. Return loss for 90° design

Figure 10. Insertion loss for 90° design

Figure 11. Phase shift for 90° design
V. CONCLUSION

The 3-bit digital RF phase shifter developed based on switched transmission line is designed for 10GHz operating frequency. The proposed design shows the performance with maximum phase shift such as 45.55°, 84.76° and 177.10° for 45°, 90°, 180° respectively. The phase error is less than 5.3°. Therefore, a maximum phase shift is obtained with minimum wavelength. With these techniques, we could achieve higher resolutions, larger phase shift per unit length and low loss phase shifters. The future work is to increase the isolation, reduce the insertion loss and mainly to increase the phase shift per unit length. This can be used for satellite, wireless communications and wideband frequency applications and it is also used in phased array antennas.

REFERENCES


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Dr. B. Nataraj completed his Ph.D in Information and Communication Engineering from Anna University, Chennai in 2015. He received his M.E. in Communication Systems from Thiagarajar College of Engineering, Madurai in 2004 and B.E. in Electronics and Communication Engineering from Thiagarajar College of Engineering, Madurai in 2002. He has 11.5 years of teaching experience. His research interest includes RF / Microwave and RF MEMS Devices. He has published more than 16 research papers in national and international journals and 13 papers in national/international conferences. He was the incharge of Signal Processing Laboratory and received a fund of Rs. 15 Lakhs towards MODROB scheme for modernizing Signal Processing and VLSI lab from All India Council of Technical Education, New Delhi. He had addressed several Guest Lectures in the area of RF Transmission Lines, RF MEMS, Microsensors, Signals and Systems and Digital Signal Processing. He has chaired sessions for various national conferences and is a reviewer member for many national and international journals. He is a member in professional societies like ISTE and AESNG.
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