Testing Embedded Systems from Multi-Output Domain Perspective

J Sasi Bhanu, JKR Sastry, M Lakshmi Prasad

Abstract: The Modern embedded systems must be highly reliable and if not the products cannot even reach the market. Testing of embedded systems must be carried from multiple perspectives which include Input Domain, Output domain, Input-Output domain, Multi-Input domain, and Multi-output domain to ensure that the embedded systems are thoroughly tested. That will ensure delivery fail free embedded systems.

Sometimes the outputs generated which are either internal or external by an embedded system are interdependent and sometimes occur as a vector of outputs. Embedded systems must be tested for proper occurrence of designed outputs in the order and the sequence expected. Test cases are to be generated that will verify whether the outputs are occurring in the order and the sequence expected. In this paper an algorithm and approach has been presented for generating test cases that when tested leads to testing embedded systems comprehensively ensuring that the outputs occur in the order and sequence expected.

Index Terms: Optimal selection procedure, Output domain, Multi-output domain, Testing Embedded Systems.

I. INTRODUCTION

Testing an embedded system thoroughly from different perspectives that include input-domain, out-put domain, Input-output domain, Multi-input domain and Multi-output domain are absolutely required. The test cases generated considering each domain must be consolidated to provide whole set of test cases that helps testing an embedded system comprehensively. The main aim of this testing is to select the acceptable test cases and detect as many faults based on requirements specification at least cost and time.

Testing an embedded system comprehensively considering every domain will help developing defect-free embedded systems which have high market value. Testing an embedded system can be carried with the help of details known externally by following Black-Box testing Approach. In this case only functional testing of the embedded system can be carried. Testing of an embedded system can be carried by using white-Box testing considering both external and internal details of the embedded systems

Embedded systems must be tested considering hardware and firmware which are used to build the embedded systems. Testing Hardware and software however can be conducted independently and then the testing is undertaken after the software is migrated to the hardware. Hardware of an embedded system includes variety of hardware devices which are interconnected. Some of the devices include microcontrollers, application-specific integrated circuits, A/D converters, Sensors, Actuators and processors. Embedded systems are wide open spanning across several kinds applications that typically include power plant systems, networking gadgets, storage systems. The firm is the software residing in the internal memory of the embedded system will run the application for which the firmware is written. To ensure that the embedded systems is perfectly developed and is fail-free, thorough testing of the hardware and firmware needs to be done.

Combinatorial testing has been proved to be the best as it produces least number of test cases that will help in comprehensively testing any system including the embedded system. Combinatorial testing can pursue considering any of the domains of a system including the output domain, input domain, input-output domain, multi-input domain and Multi-output domain

Combatorial testing may simply involve inputs, outputs and Input-output relationships. For a few safety critical embedded systems, building test cases drawn from output domain will be more suitable than from input domain as it guarantees that all or as many possible output combinations are comprehensively tested. Sometimes outputs from an embedded system either happen in unison or as inter-dependent vector of outputs generally called as Multi-output Vector. To arrive at the inter-dependency between the outputs, one has to consider the external outputs and internal outputs that cause the external outputs. Testing an embedded system from multi-output perspective is quite important to ensure that the internal systems are functioning quite effectively that are responsible for generating specific outputs in the order sequence required.

A pilot project has been developed for monitoring and controlling the temperatures (TMCNRs) within the Nuclear reactor systems. This system produces several outputs that are inter-dependent and the outputs must happen in proper order and sequence. In this system, operation of pumps and buzzers is independent. But the status of the pumps and buzzers depend on the input temperatures detected by the temperature sensors. Test cases should be generated to verify the functionality of all the pumps and buzzers and the generation of the outputs must be in-line with internally generated signals.

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Dr. J Sasi Bhanu, Siddhartha Institution of Technology and Sciences, Narapally, Hyderabad, Telangana.

Dr. JKR Sastry, Koneru Lakshmaiah Education Foundation, Vaddeswaram, Guntur District, AP, India

Dr. M Lakshmi Prasad, Koneru Lakshmaiah Education Foundation, Vaddeswaram, Guntur District, AP, India.
In this paper, the authors have proposed a method called Optimal Selection Procedure (OSP) that generates the test cases by choosing certain combinations of outputs based on the relationships that exists among the output variables. The optimal selection procedure has been applied to generate the test cases that help testing TMCNRS considering various outputs generated by the system that include status of pumps, and buzzers and the internal signals generated.

II. RELATED WORK

Genetic algorithms have been used by Chandra Prakash et al., [1, 2] for generating test cases in the criticality regions of embedded systems. Genetic algorithms have been used in conjunction with combinatorial methods, Output domain of an embedded system that controls temperatures within nuclear reactor has been considered and they have shown how exhaustively test cases have been generated in the criticality regions of the embedded systems. They have employed pseudo-exhaustive test case generation method to generate test cases that can find many faults at different levels. The generator developed by them considered all combinations of outputs and their relationships with inputs. The number of test cases generated through the method proposed by them is found to be quite less compared to other methods.

Neural network based methods are in use for solving several problems including its use for generating test cases or for conducting actual testing. Ruilian Zhao et al., [3] has combined genetic algorithms and neural networks for generating test cases. They have created a function model that substitutes software. The function model identifies several computing nodes that represent hidden nodes in the neural network. Genetic algorithm is used to find the corresponding inputs using the outputs generated through neural network model.

There is interdependence among various faults that can occur while software is in execution. It becomes difficult to test software when faults occur due to occurrence of many other faults. It has been presented by D. Richard Kuhn et al., [4] that few of the conditions built into the software causes many of the faults considering many of the domains for which software is written. Selecting combinations of inputs that can cover all kinds of faults that can occur will lead to generation of test cases that can exhaustively test software. However, the behavior of the software involves complex event sequences it is difficult to find accurate input combinations. Covering arrays is another method that has been advanced quite rapidly. Two methods have been proposed by D. Richard et al., [5] that include covering arrays that represents interactions among the input variables and the method model checking using which the test cases are generated. They have tested the method on a traffic collision avoidance system. They have considered 6 way interactions among the input variables. Several tools existed for generating combinatorial test cases. R. Kuhn et al. [6] have presented a commission of various methods using real-world applications. They have presented empirical analysis of the methods.

BREEDING TEST CASES THROUGH GENETIC ALGORITHMS

Breeding test cases through genetic algorithms have been presented by D. Berndt, J. Fisher et al., [7]. The test cases can be breaded based on severity, novelty and proximity. A fitness function has been presented that reveal the behavior of microorganisms. They have presented the technique through a case study and using several visualization techniques using which the behavior is analyzed using fossil records. Branch and find fault method along with genetic algorithm has been used for generating the test cases by B. F. Jones et al., [8]. The test cases generated by that method are fewer in number when compared to ransom testing. The methods also cover entire software. However, the computation effort in this case is very high. The test cases generated are very close to the sub-domains of inputs. Genetic algorithms have been used for exploiting the faults that can be caused when branching takes place within the software execution.

Adequate testing has to be carried to ensure entire coverage of the software requiring more number of test cases. Automated tools are required to generate adequate number of test cases. One such tool has been presented by Kamal Zuhairi Zamli et al [9]. The tool presented them uses combinatorial approach for generating the test cases. They have considered many combinations of inputs, environments on which the hardware and software is used and many of the system conditions for generating test cases through combinatorial methods. The conformance of the test cases against the system specification has also been shown by the authors.

A testing complex system is a challenge. A kind of system is required for testing software systems. Once unit testing is carried, building on it generates test cases for carrying system level testing is possible by suing the genetic algorithms. The test cases used for unit testing can be used as test case generation seeds. Watkins et al [10] have presented vocabulary of attributes and its related framework for generating test cases. The have also presented some visualization techniques using which one can find the system failures right in the Initial stages.

Pair wise testing method is quite frequently used combinatorial method for generating test cases. Huge number of test pairs gets created when number input variables are quite high. Meta- heuristic search techniques are required to find the pairs that are minimum and lead optimum number of test cases. Xiang Chen et al., [11] have used PSO (Particle swarm optimization) algorithm to find optimum input pairs. They have presented many algorithms based on PSO to systematically generate all the test cases. They have used two strategies that include IPO-Like and the other one-at-a-time strategy. In both these algorithms PSO is used for generating the test cases. They have also presented a method using which the search space can be defined. PSO also has been used by B.S. Ahmed and K.Z. Zamli [12] for generating test cases using a t-way strategy. They compared test size reduction achieved by using this method against other similar methods presented in the literature. Some improved algorithms in similar lines have been presented by Jiang Shouda [13].

Particle Swarm techniques is one moth important technique that is being used extensively for implementing various applications.
A t-way testing method based on particle swarm method has been proposed by K. Rabbi, et al., [14]. They have implemented a testing strategy for generating optimum number of test cases. They have also presented a comparative analysis and shown that particle swarm method produced very few but effective number of test case.

Several strategies exists for carrying combinatorial testing. A survey has been conducted by Abdul Rahman [15] and presented several test strategies that considers the Input-output domain if an embedded system. They have compared all the available algorithms with nature inspired algorithm considering all input-output relations. They have presented that combinatorial testing methods can also be used for testing software that uses extensively logical and Boolean expressions. Testing software that is predominantly developed using Boolean expressions has been presented by S. Vilkomin [16].

A pair-wise test case generation method has been used by Deepa Gupta [17]. The have shown how test cases can be generated considering the input sequences. The have shown how input sequences can be explored for generation of test cases. This approach makes sure the generation of test cases that are required all relations between every pair of instructions at least once.

A two stage annealing algorithm that has been simulated has been presented by Jose Torres-Jimenez et al., [18]. The algorithm constructs the ternary covering arrays that help generating the test cases which can be used for testing the embedded systems comprehensively. Combinatorial test data generation method has been presented that focusses on process, approach and constraints by S. Route [19]. They have employed IBM developed tool for generating the test cases based on the combinations of the inputs and optimization of test cases and to reduce the effort required to comprehensively test embedded systems.

Use case artifacts are the best models that can be used for generating the test cases based on combinatorial methods. A rule based model has been used for finding the input domain for use case models P. S et al., [20]. They have implemented two parsers that are natural language and XML based. The rules that are formulated have been using for generating the output domain of a given system.

Several testing tools have been developed by Y. Yao et al., [21] based on combinatorial methods. They have considered several testing scenarios comprising of industrial, and academic domains. They have developed several testing tools that deal with test case optimization, test case generation etc. The tools developed by them are scalable, configurable and modular.

Many contributions have been presented for testing the software [22][23][24][25][26][27][28][29][30][31][32] but most of the contributions have been focused at input perspective only.

III. PILOT EMBEDDED SYSTEM

A prototype model has been developed for monitoring controlling the temperatures within a nuclear reactor system. The system is famously called as TMCNRS system. The Block diagram of the system is shown in 1. Temperatures within the nuclear reactor tubes are measured through the sensors that are mounted on to the tubes. The analog signals synonymous to the temperatures sensed are sent to an A/D converter after amplifying the same through operational amplifiers. The temperatures within the nuclear reactors are read through firmware that has been installed within a microcontroller system to which A/D converter has been connected. The temperatures read are stored in the RAM in digital form.

Two pumps are interface to the Micro controller system through relays using which the pump start stop operations are controlled. The pumps when started will pump a colorant into a nuclear reactor so that temperatures with the reactor tubes will be brought down to the reference temperatures. The pumps are made to pump the coolant into the tubes when the sensed temperatures are more than the reference temperatures. A buzzer is connected to the Micro Controller which will be triggered when absolute of difference two sensed temperatures is > 2. Maintenance of proper temperatures gradients is one of the most important crucial issues for proper enrichment of uranium. The Micro controller is connected to a PC through RS232C interface for feeding the reference temperatures.

The nuclear reactor system is a safety critical system. The system is built through authentication and authorizations system. The passwords are fed through a keyboard connected to the Micro controller system. The microcontroller based system is connected with an LCD for displaying messages that show the status of process taking place within the nuclear reactor system. The alerts to the operator are also displayed through LCD. The functional requirements of the TMCNRS system are shown in Table I. Table II shows the relationships among the Input-output variables.

![Fig. 1 System Integration Diagram of TMCNRS](image-url)
Table I. Requirement condition of TMCNRS

<table>
<thead>
<tr>
<th>S. No.</th>
<th>Functional requirements</th>
</tr>
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<tbody>
<tr>
<td>Req 1</td>
<td>Must be able to read the reference temperatures through a PC connected to the Micro Controller through an RS232C interface</td>
</tr>
<tr>
<td>Req 2</td>
<td>Within every 10 Milli secs Temp-1 must be sensed, sent to HOST and displayed on LCD. PUMP-1 must be made to run when TEMP1- &gt; reference temp-1 or PUMP-1 must be stopped</td>
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<tr>
<td>Req 3</td>
<td>Within every 10 Milli secs Temp-2 must be sensed, sent to HOST and displayed on LCD. PUMP-2 must be made to run when TEMP2-&gt; reference temp-1 or PUMP-1 must be stopped</td>
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<tr>
<td>Req 4</td>
<td>If the absolute difference between the sensed temperatures &gt; 2 then the buzzer must be triggered or lease the buzzer must be stopped</td>
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</table>

Table II Relationship between input parameters and status of Pump and buzzer outputs

<table>
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<tr>
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<tbody>
<tr>
<td>30</td>
<td>31</td>
<td>0 (Pump OFF)</td>
<td>0 (Pump OFF)</td>
<td>0 (Buzzer OFF)</td>
</tr>
<tr>
<td>20</td>
<td>33</td>
<td>0 (Pump OFF)</td>
<td>1 (Pump ON)</td>
<td>1 (Buzzer ON)</td>
</tr>
<tr>
<td>33</td>
<td>30</td>
<td>1 (Pump ON)</td>
<td>0 (Pump OFF)</td>
<td>1 (Buzzer ON)</td>
</tr>
<tr>
<td>32</td>
<td>33</td>
<td>1 (Pump ON)</td>
<td>1 (Pump ON)</td>
<td>0 (Buzzer OFF)</td>
</tr>
</tbody>
</table>

IV. GENERATION OF TEST CASES FOR TMCNRS USING OPTIMAL SELECTION PROCEDURE

The first step in the selection procedure is to find Output Domain. In the current scenario, we have two output domains that have to be covered, one is Pump Status Code (PSC) i.e. in the above system OSC is pump and another is Buzzer Status Code (BSC).

As mentioned in [1], 28 test cases can be generated to cover all the BSC codes and 27 test cases can be generated to cover all the OSC codes for 8 sensors and 7 buzzer TMCNRS system. Authors in [1] suggested pseudo-exhaustive testing wherein, test cases are generated to cover all OSC codes and then test cases are generated to cover all BSC codes. So, total of 28+27 test cases are required to cover all possible outputs in multi output system. Algorithm 1 relates to covering all possible BSC codes, and Algorithm 2 to cover all possible OSC codes.

Algorithm 1:

This algorithm explains how to derive the test case from the given BSC code. Assuming buzzer code will be 1 when absolute difference of consecutive temperature sensors is more than 2 (assumed Gradient value), otherwise 0.

Let BSC be a vector containing n-1 binary values each representing a set of bits (b1, b2, b3…bn-1) where bi (i=1, 2…(n-1)), takes value either 0 or 1.

Let PSC be a vector containing n binary values, each represented by a set of bits showing the status of the pumps related to specific temperatures ti (i=1, 2…n).

As the buzzer output depends on the absolute difference of adjacent temperatures, Let the first temperature value is selected randomly from 1 to 99 and second value ti (i= 2…n) is derived from the following equation. First temperature is defined as t1= random (1, 99), then the temperatures ti (i= 2…n) is calculated as

\[ t_i - t_{i-1} + \text{Random} \left[ -2, 2 \right] \text{ if } b_i = 0 \]

Random [1, 99] such that |Random [1, 99] - ti-1| > 2 \text{ if } b_i = 1 \text{ for } 2 \leq i \leq n (1)

Initialize BSC array = \{0, 1, 2, 3..., (2n-1)-1\} of a multi output embedded system each value representing a binary value

While (No. of BSC elements) > 0

Get a BSC code from BSC array.
Generate the test case from the above BSC code using equation 1.

Remove the selected BSC code from the BSC array. Decrement the number of BSC elements.

\}

Algorithm-2:

This algorithm tells how to derive the test case from the given PSC code based on the reference temperature inputs.

Let \((Ref_1, Ref_2, \ldots, Ref_n)\) be the Reference temperature of the sensors used in the system.

Let \((p_1, p_2, p_3, \ldots, p_n)\) be the given PSC code, where \(p_i (i=1, 2, \ldots, n)\) takes value either 0 or 1. \((t_1, t_2, t_3, \ldots, t_n)\) be the test vector being generated from the given PSC code and reference temperatures.

\[
\text{Random } [1, \text{Ref}_i] \text{ if } p_i = 0
\]

\[
t_i = \text{Random } [\text{Ref}_{i+1}, 99] \text{ if } p_i = 1 \leq i \leq n
\]

Initialize OSC array = \{0, 1, 2, 3, \ldots, (2n-1)\} of a multi output embedded system

While (no. of OSC elements > 0)

\}

Get an OSC code from OSC array.

In similar lines, we proposed yet another algorithm which derives test cases that covers all possible BSC and OSC codes together till all BSC codes are covered, by properly choosing the OSC code and BSC code based on Feasibility Table as shown in table 3. Test cases for the left over OSC codes are generated using Pseudo code 2.

Algorithm 3:

This algorithm tells how to derive the test case from the given OSC code and BSC code based on the reference temperature inputs

Step 1:

Let \((Ref_1, Ref_2, Ref_3, \ldots, Ref_n)\) be the Reference temperature of the sensors used in the system.

Let \((p_1, p_2, p_3, \ldots, p_n)\) be the given PSC code, where \(p_i (i=1, 2, \ldots, n)\) takes value either 0 or 1. Assuming buzzer code will be 1 when absolute difference of consecutive temperature sensors is more than 2, otherwise 0.

Let \((b_1, b_2, b_3, \ldots, b_{n-1})\) be the given BSC code, where \(b_i (i=1, 2, \ldots, (n-1))\) takes value either 0 or 1. \((t_1, t_2, t_3, \ldots, t_n)\) be the test vector being generated from the given PSC code and reference temperatures.

Before proceeding to generate the test case using BSC code and PSC code together, we should check the feasibility of the existence of these two codes first from Table III. Random \([x, y]\) generates the random number between \(x\) and \(y\). This is achieved in the program using rand () function as \(x+ \text{rand}() \% (y-x+1)\).

\[
\text{TABLE III FEASIBILITY CHECK TABLE
}
\]

\[
\begin{array}{|c|c|c|c|}
\hline
\text{pi} & p_{i-1} & \text{Feasibility} & \text{Formula} \\
\hline
0 & 0 & 0 & \checkmark \quad t_i = \text{Random}[1, \text{Ref}_i] \quad \text{such that } t_i < \text{Ref}_i \text{ for } 1 \leq i \leq n \\
0 & 0 & 1 & \checkmark \quad t_i = \text{Random}[1, \text{Ref}_i], \text{Ref}_{i-1} \quad \text{such that } R_{i-1} < t_i < \text{Ref}_i \text{ for } 1 \leq i \leq n \\
0 & 1 & 0 & \times \quad \text{Not Feasible} \\
0 & 1 & 1 & \checkmark \quad t_i = \text{Random}[\text{Ref}_{i-1}+1, 99] \quad \text{such that } t_i > t_{i-1}\quad \text{for } 1 \leq i \leq n \\
1 & 0 & 0 & \times \quad \text{Not Feasible} \\
1 & 0 & 1 & \checkmark \quad t_i = \text{Random}[\text{Ref}_i-1, 99] \quad \text{such that } t_i < t_{i-1}\quad \text{for } 1 \leq i \leq n \\
1 & 1 & 0 & \checkmark \quad t_i = \text{Random}[\text{Ref}_i+1, 99] \quad \text{such that } t_i > t_{i-1}\quad \text{for } 1 \leq i \leq n \\
1 & 1 & 1 & \checkmark \quad t_i = \text{Random}[\text{Ref}_{i-1}+1, 99] \quad \text{such that } t_i > t_{i-1}\quad \text{for } 1 \leq i \leq n \\
\hline
\end{array}
\]

test cases for covering the output domain and then uses the same generating the test cases based on input domain.

This kind of selection of PSC code and BSC code together is not possible. i.e. \(p_i = 0; p_{i+1} = 1\) and \(b_i=0\). Table 3 tells about the feasibility of the possible values of \(p_i\) and \(b_i\).
Testing Embedded Systems from Multi-Output Domain Perspective

PSC code (p1, p2, p3, p4) and BSC code (b1, b2, b3) is said to feasible: If pi, pi+1 and bi satisfies the above table for each i = 1,...,n-1 (3)

Step-2:
Optimal Selection of test case using Feasibility Table. In this section, we will discuss about how to derive the test vector i.e. (t1, t2, t3…tn) satisfying both the OSC and BSC code.

Let us consider an example of PSC code = (1, 0, 0, 1) and BSC code = (1, 0, 1) which means pumps p1, p4 should be ON and p2, p3 should be OFF, and the buzzers b1, b3 should be ON and b2 should be OFF.

We can observe that the selected PSC code and BSC code is feasible from equation (3). For the given example, p1=1, p2=0 and b1=1 and from the feasibility table, we can get t1 and t2 as below.

t1=Random [Ref1+1, 99]
t2=Random [1, Ref2] such that |t1 - t2| > 2

Next step is to derive t3 from the table t3=t2+ Random [-2, 2] such that t 3 ≤ Ref3. Second part of the algorithm derives test case from the leftover PSC codes from the above step. For simplicity, the PSC codes and BSC codes in the respective arrays are denoted in decimal form rather than in the binary form.

Main Algorithm:

Initialize PSC array = {0, 1, 2, 3..., (2n-1)}
Initialize BSC array = {0, 1, 2, 3..., (2n-1)-1}

While (no. of BSC elements>0)
{
    For each BSC code, find a PSC code which is feasible based on feasibility table.
    Use the above BSC code and PSC code generate the test case using Algorithm 3.
    Remove the BSC code from BSC array
    Decrement the no. of BSC elements by 1
    Remove the PSC code from PSC array
    Decrement the no. of PSC elements by 1
}
While (no. of PSC elements >0)
{
    Get a PSC code from PSC array.
    Generate the test case from the above PSC code using equation 2.
    Remove the selected PSC code from the PSC array.
    Decrement the no. of PSC elements.
}

With the first part of algorithm, we get the test cases that coverall the possible BSC codes (2n-1) along with partial coverage of PSC codes (2n-1). And we will be left with another 2n-1 PSC codes in the PSC array. Second part of the algorithm derives test case from the leftover PSC codes from the above step. For simplicity, the PSC codes and BSC codes in the respective arrays are denoted in decimal form rather than in the binary form.

V. EXPERIMENTAL RESULTS

In this paper Optimal Selection Procedure (OSP) is implemented to derive the test cases for multi output domain that can be considered with respect to an embedded system. The optimized test suit derived from the multi output domain of TMCNRS with a configuration of 4-Sensors, 4-Pumps, 3-buzzers using optimal selection procedure is shown in Table IV. The optimized test suit derived for TMCNRS with a configuration of 8-Sensors, 8-Pumps, 7-buzzers using optimal selection procedure is shown in Table V.

In order to calculate the effectiveness of this approach, the results have been compared with the results generated by the Genetic algorithm [1] and particle swarm optimization technique also. This approach yields to produce a less number of test cases with high efficiency in terms of time when compared to the previous existing technique i.e. genetic algorithms and particle swarm optimization as shown in Table VI. Execution time for various input parameters using 64 bit windows system is shown in Table VII.

TABLE IV OPTIMIZED TEST SUITE GENERATED FROM MULTI OUTPUT DOMAIN OF TMCNRS USING OSP

<table>
<thead>
<tr>
<th>Test Case</th>
<th>Tempel 1</th>
<th>Tempel 2</th>
<th>Tempel 3</th>
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<th>Tempel 6</th>
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</tr>
</tbody>
</table>

PSC (Binary) | BSC (Binary)
-------------|-------------
0             | 00000000    
0             | 00000000    
1             | 00000000    
0             | 00000010    
1             | 00000011    
0             | 00000020    
1             | 00000021    
0             | 00000030    
1             | 00000031    
0             | 00000040    
1             | 00000041    
0             | 00000050    
1             | 00000051    
0             | 00000060    
1             | 00000061    

TABLE V OPTIMIZED TEST SUITE GENERATED FROM MULTI–OUTPUT DOMAIN OF TMCNRS
VI. CONCLUSION
Input domain and output domain based combinatorial testing using combinations of inputs and outputs have been proved to be effective methods when combined with genetic algorithms or heuristic search methods. There are many applications that produce clusters of output called Multi-output domain. The output domain as such can be recognized as clusters of output vectors, each vector having common characteristics. In such situations Optimal Selection Procedure presented in this paper will generate most effective test cases that cover 100% of the source code. In the example used for showing the effectiveness of the procedure, two output vectors have been chosen. While one output vector shows the status of pumps, the second vector deals with the status of buzzers.

VII. ASSOCIATION BETWEEN PSEUDO EXHAUSTIVE TESTING USING GA, PSO AND OPTIMAL SELECTION PROCEDURE

<table>
<thead>
<tr>
<th>Test case No.</th>
<th>Input Vector</th>
<th>Expected Output</th>
</tr>
</thead>
<tbody>
<tr>
<td>(Ref.Temp=30)</td>
<td>(Ref.Temp=32)</td>
<td>(Ref.Temp=34)</td>
</tr>
<tr>
<td>Temp1</td>
<td>Temp2</td>
<td>Temp3</td>
</tr>
<tr>
<td>Temp1</td>
<td>Temp2</td>
<td>Temp3</td>
</tr>
<tr>
<td>(Binary)</td>
<td>(Binary)</td>
<td></td>
</tr>
<tr>
<td>1.</td>
<td>12</td>
<td>12</td>
</tr>
<tr>
<td>2.</td>
<td>30</td>
<td>31</td>
</tr>
<tr>
<td>3.</td>
<td>15</td>
<td>13</td>
</tr>
<tr>
<td>4.</td>
<td>3</td>
<td>4</td>
</tr>
<tr>
<td>5.</td>
<td>22</td>
<td>50</td>
</tr>
<tr>
<td>6.</td>
<td>18</td>
<td>51</td>
</tr>
<tr>
<td>7.</td>
<td>10</td>
<td>40</td>
</tr>
<tr>
<td>8.</td>
<td>26</td>
<td>78</td>
</tr>
<tr>
<td>9.</td>
<td>90</td>
<td>30</td>
</tr>
<tr>
<td>10.</td>
<td>41</td>
<td>32</td>
</tr>
<tr>
<td>11.</td>
<td>48</td>
<td>29</td>
</tr>
<tr>
<td>12.</td>
<td>59</td>
<td>28</td>
</tr>
<tr>
<td>13.</td>
<td>69</td>
<td>74</td>
</tr>
<tr>
<td>14.</td>
<td>72</td>
<td>66</td>
</tr>
<tr>
<td>15.</td>
<td>32</td>
<td>64</td>
</tr>
<tr>
<td>16.</td>
<td>44</td>
<td>55</td>
</tr>
</tbody>
</table>

TABLE VII. EFFICIENCY OF OSP WITH RESPECT TO EXECUTION TIME (SECONDS)

<table>
<thead>
<tr>
<th>S.N.</th>
<th>Name of the Technique</th>
<th>For 4-Pumps 3-Buzzers Config.</th>
<th>For 6-Pumps 5-Buzzers Config.</th>
<th>For 8-Pumps 7-Buzzers Config.</th>
<th>For 10-Pumps 9-Buzzers Config.</th>
</tr>
</thead>
<tbody>
<tr>
<td>1.</td>
<td>Genetic Algorithms</td>
<td>0.007</td>
<td>0.75</td>
<td>5.09</td>
<td>119.91</td>
</tr>
<tr>
<td>2.</td>
<td>Particle Swarm Optimization (PSO)</td>
<td>0.004</td>
<td>0.216</td>
<td>3.18</td>
<td>22.35</td>
</tr>
<tr>
<td>3.</td>
<td>Optimal Selection Procedure (OSP)</td>
<td>0.004</td>
<td>0.013</td>
<td>0.076</td>
<td>0.476</td>
</tr>
</tbody>
</table>

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AUTHORS PROFILE

Dr. J Sasi Bhanu is a Professor working at Siddhartha Institute of Technology and Sciences Narapally, Hyderabad She is B.TECH, M.TECH and Ph. D from Computer Science and Engineering, JNTU Kakinada. She has been specializing in the field of Embedded Systems, Web Technologies and Software Engineering. She has to her credit more than 45 papers published in reputed journals which are Scopus and SCI Indexed

Dr. JKR Sastry a double doctorate in CSE, and Management and works for KLEF University as a Professor. He has published more than 245 papers which are indexed into Scopus, SCI and Google

Dr. M. Lakshmi Prasad is a recent Ph. D from KLEF University and specializes in the area of Combinatorial testing of Embedded Systems.

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